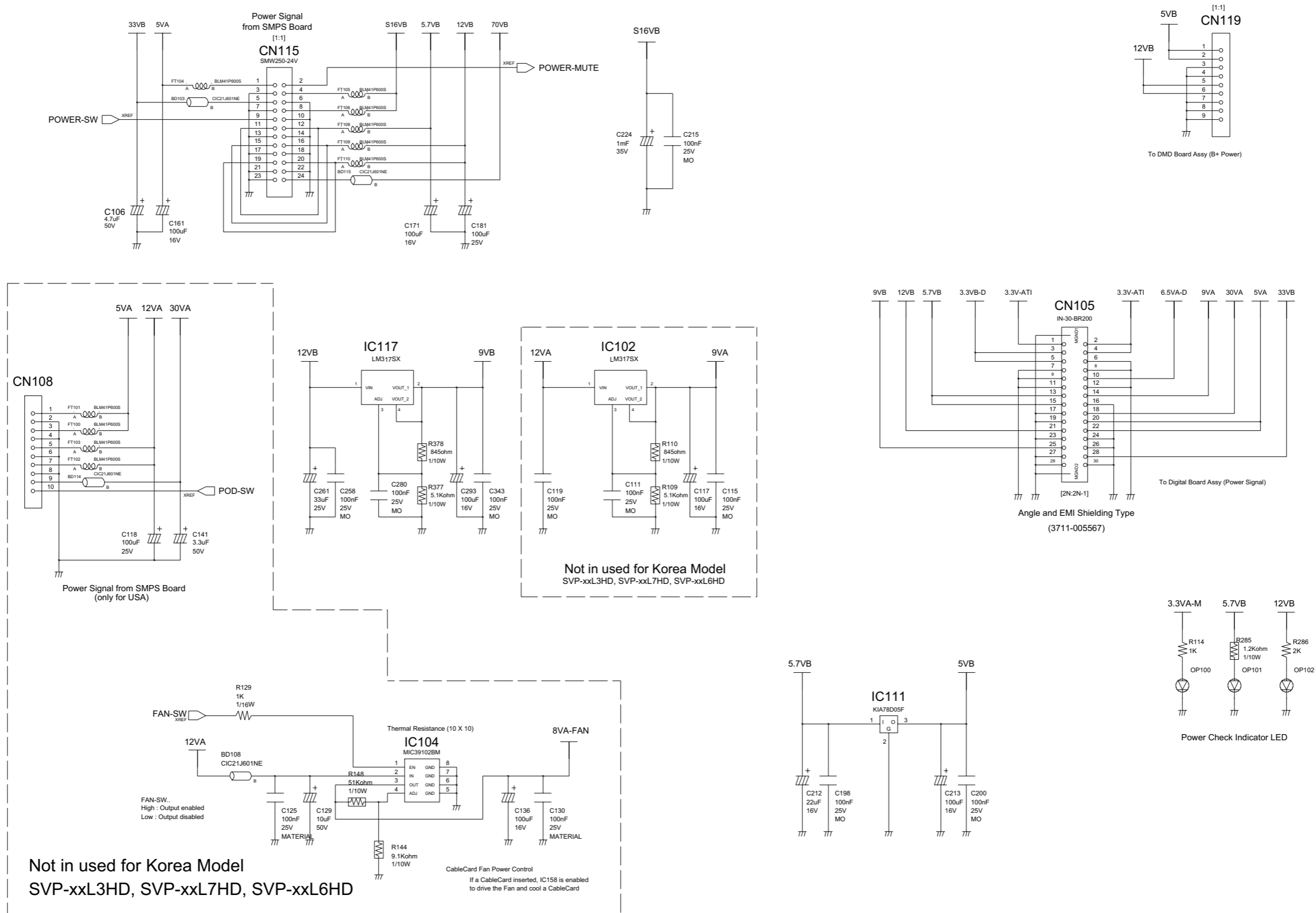


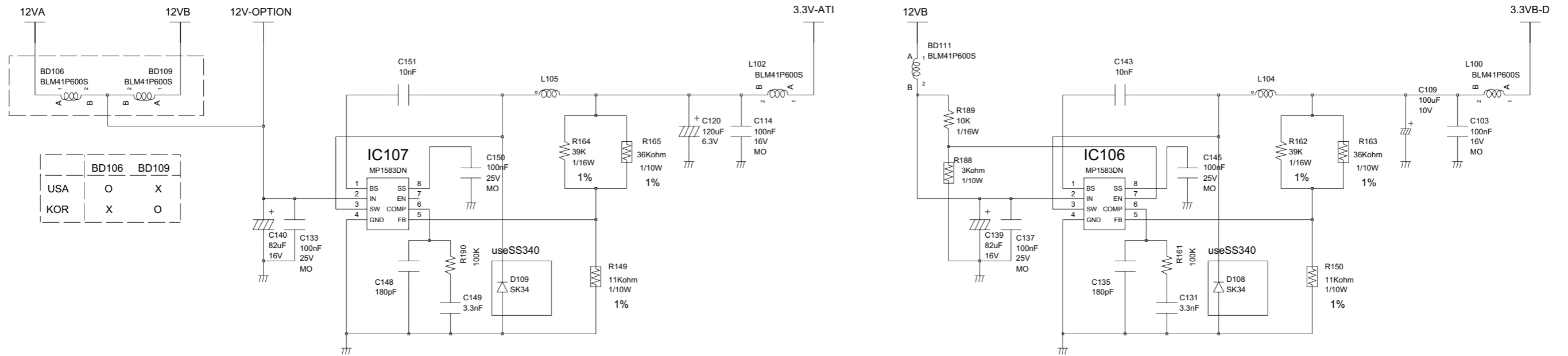
# 10. Schematic Diagram

## 10-1 Analog Board

### 10-1-1 PowerSignal



10-1-2 DCDC

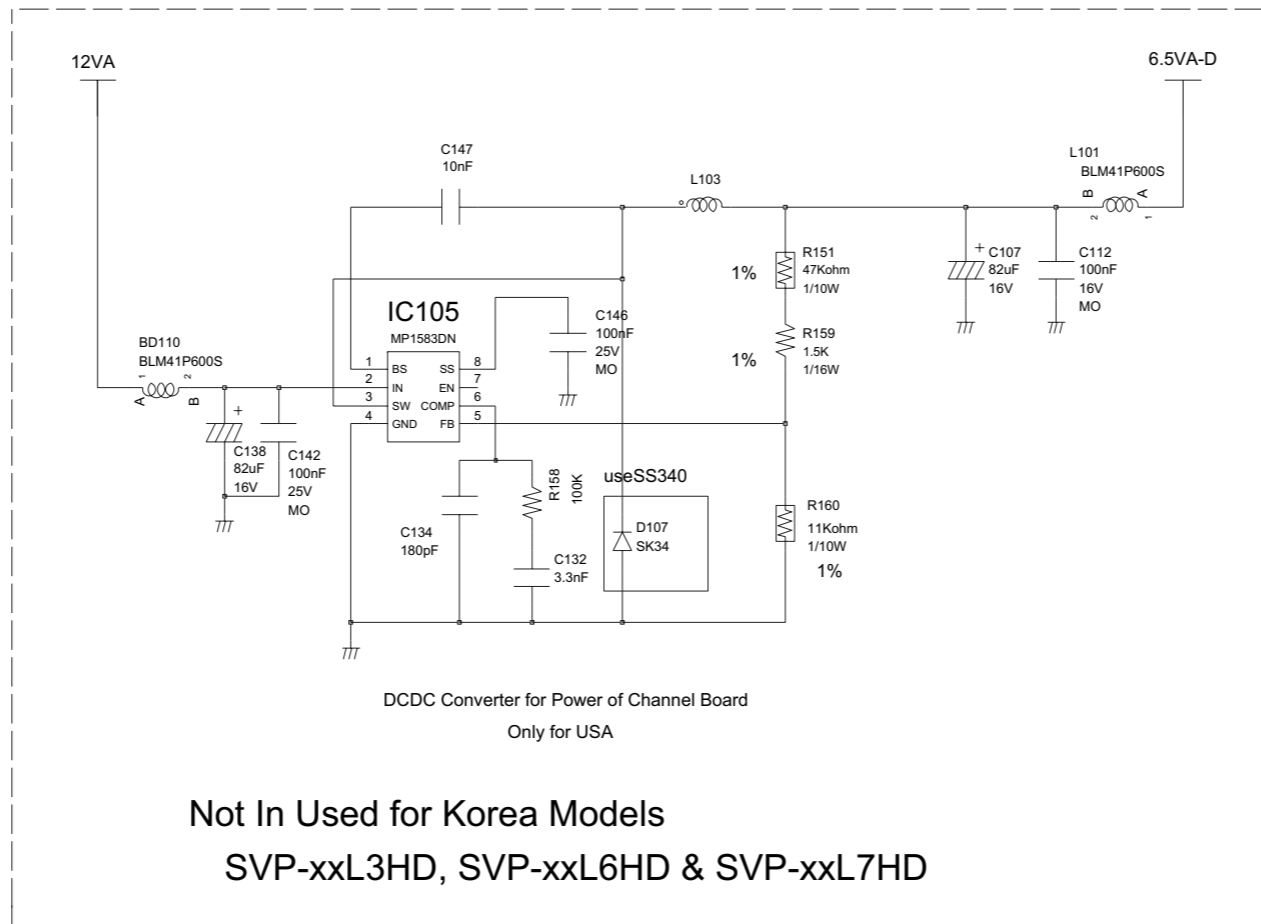


DCDC Converter for Power of ATI-CPU

USA : STD power  
KOR : B+ power

DCDC Converter for Power of ATI-CPU

USA/KOR Common B+ power



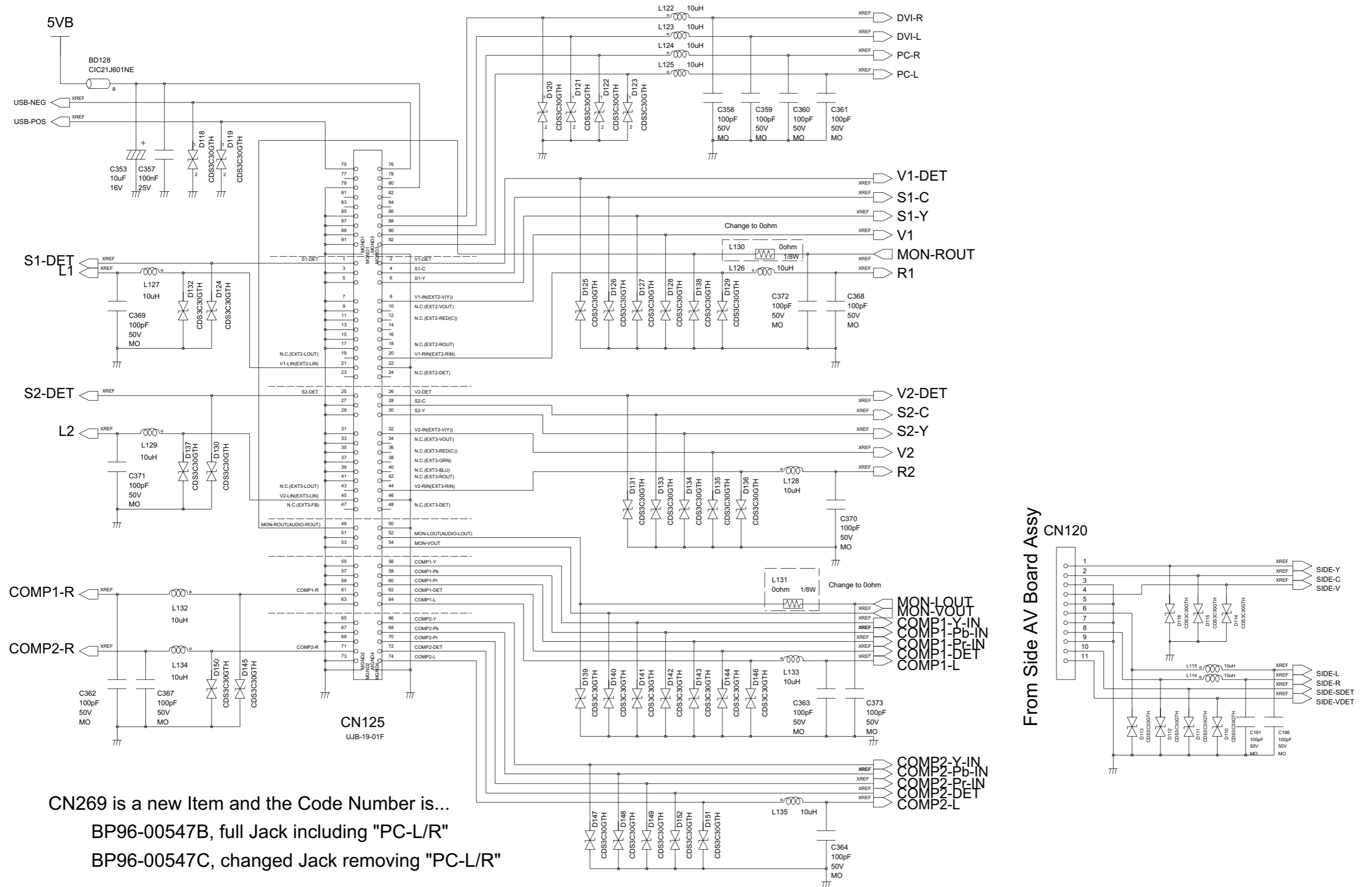
DCDC Converter for Power of Channel Board

Only for USA

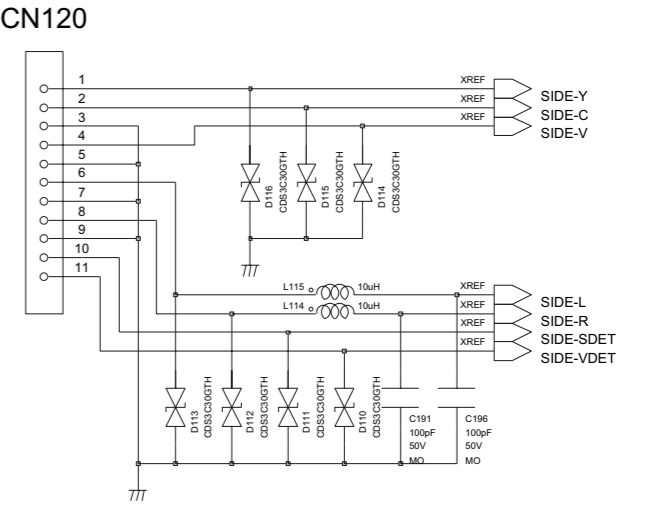
Not In Used for Korea Models

SVP-xxL3HD, SVP-xxL6HD & SVP-xxL7HD

10-1-3 AV\_Input

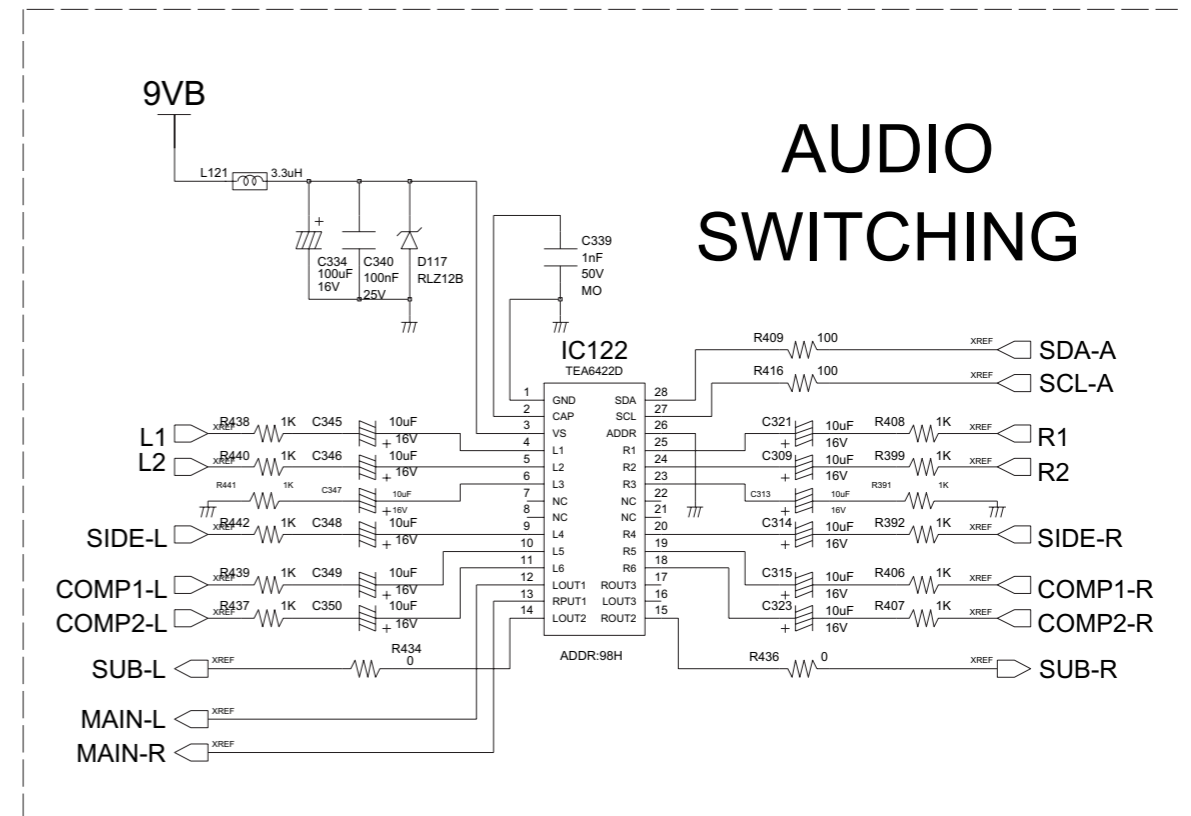
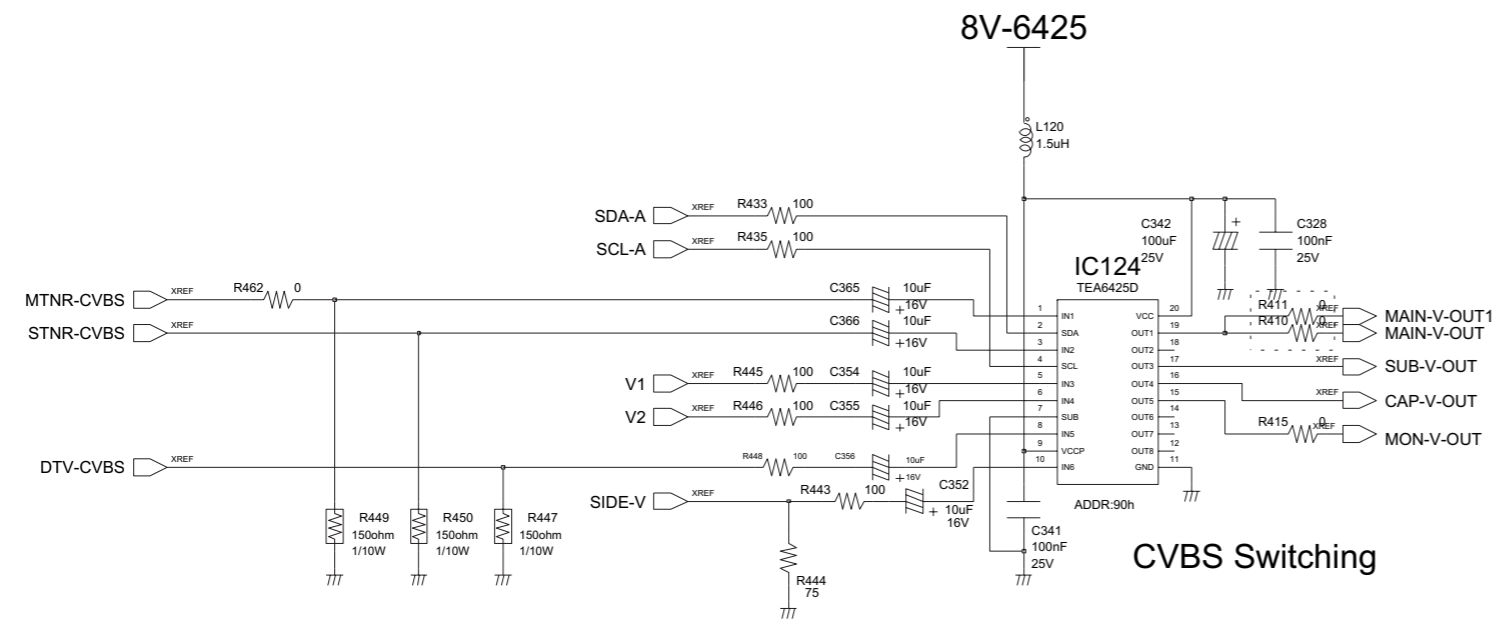
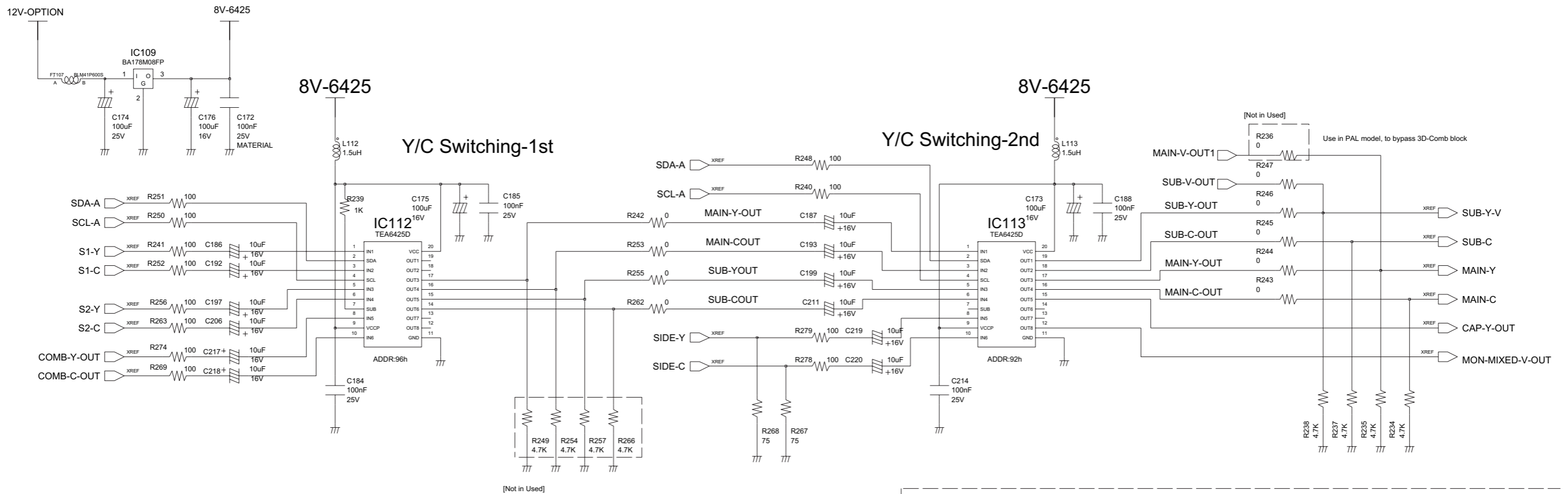


From Side AV Board Assy

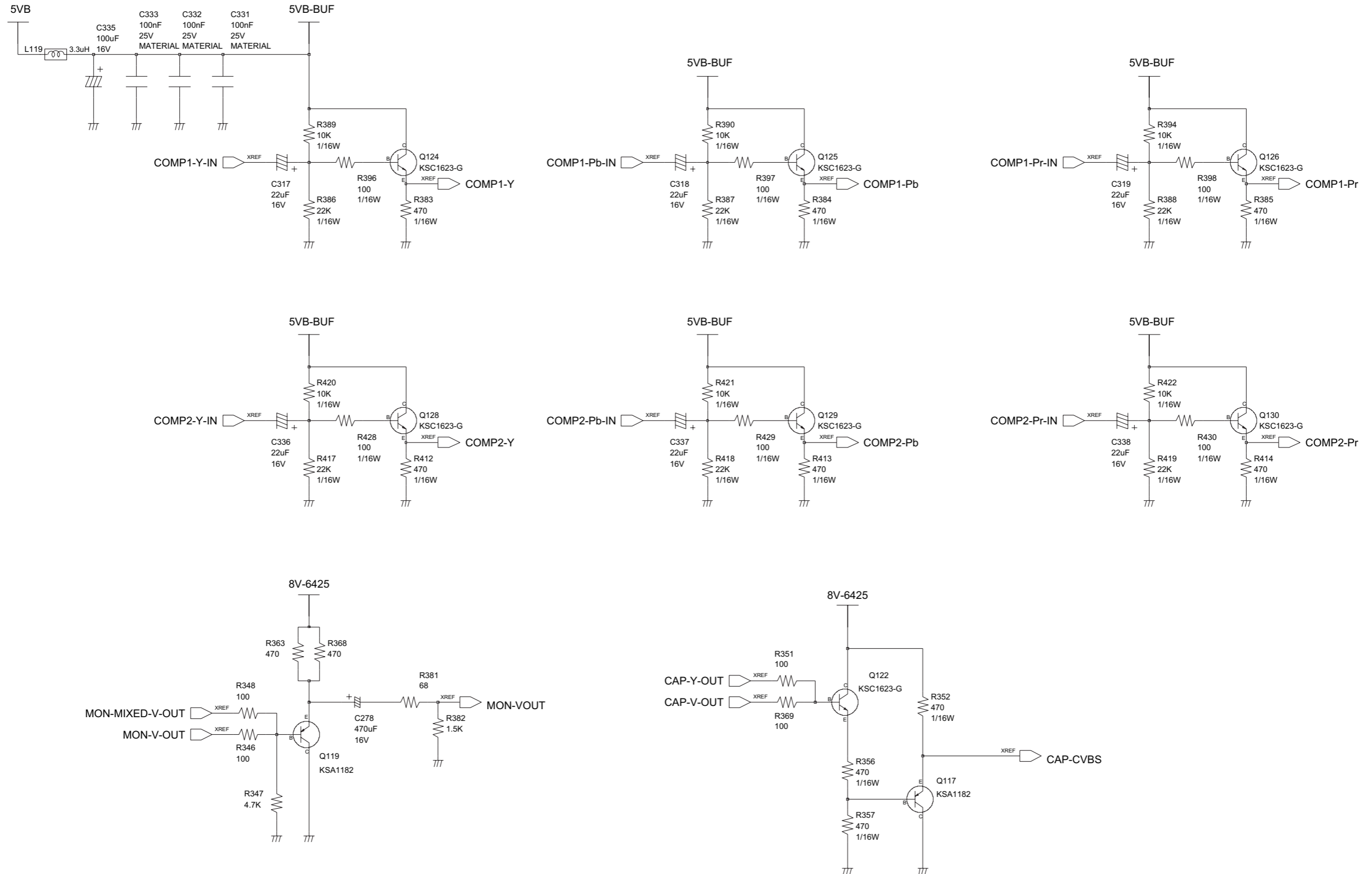


CN269 is a new Item and the Code Number is...  
 BP96-00547B, full Jack including "PC-L/R"  
 BP96-00547C, changed Jack removing "PC-L/R"

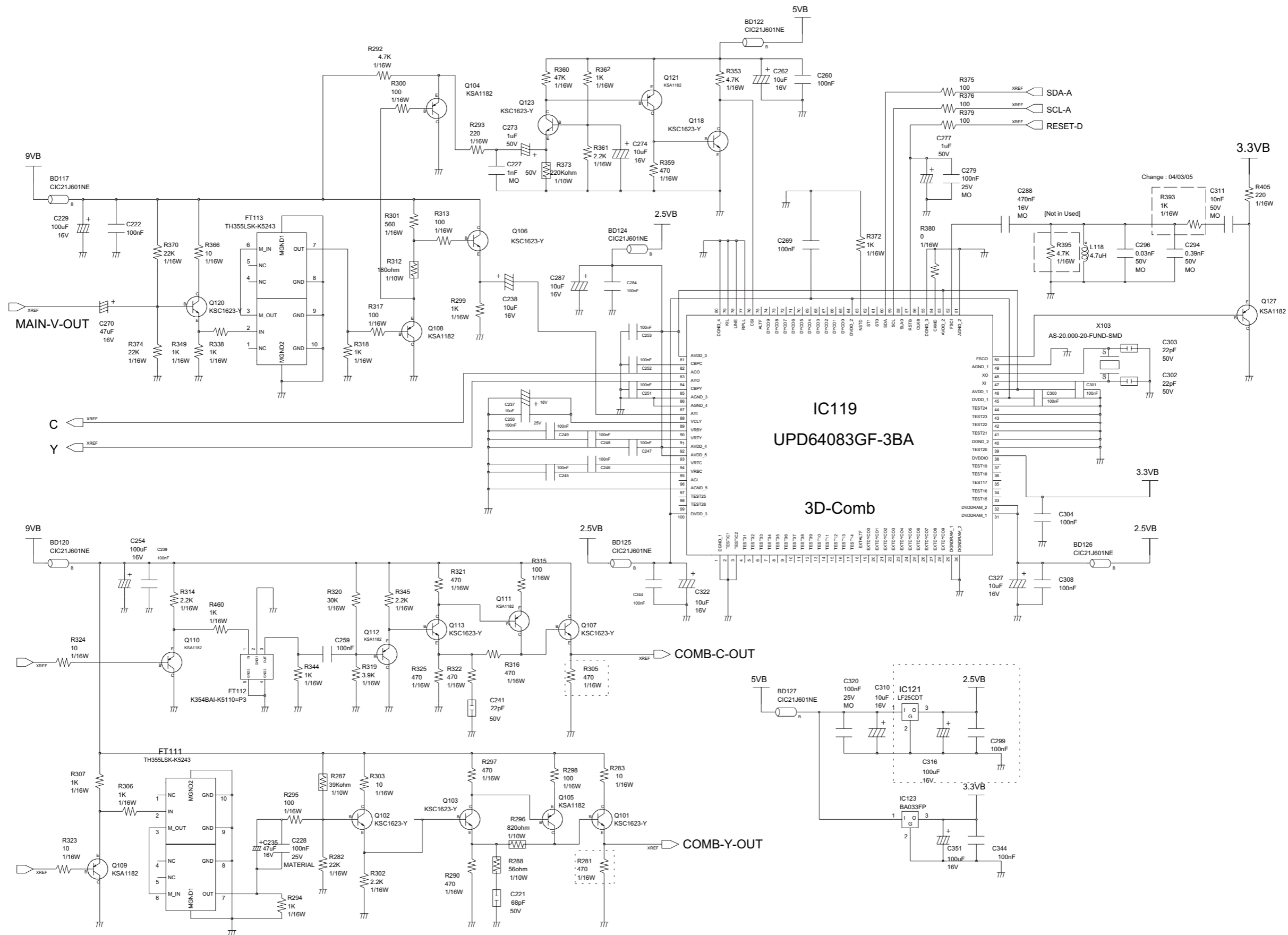
10-1-4 A/V Switching



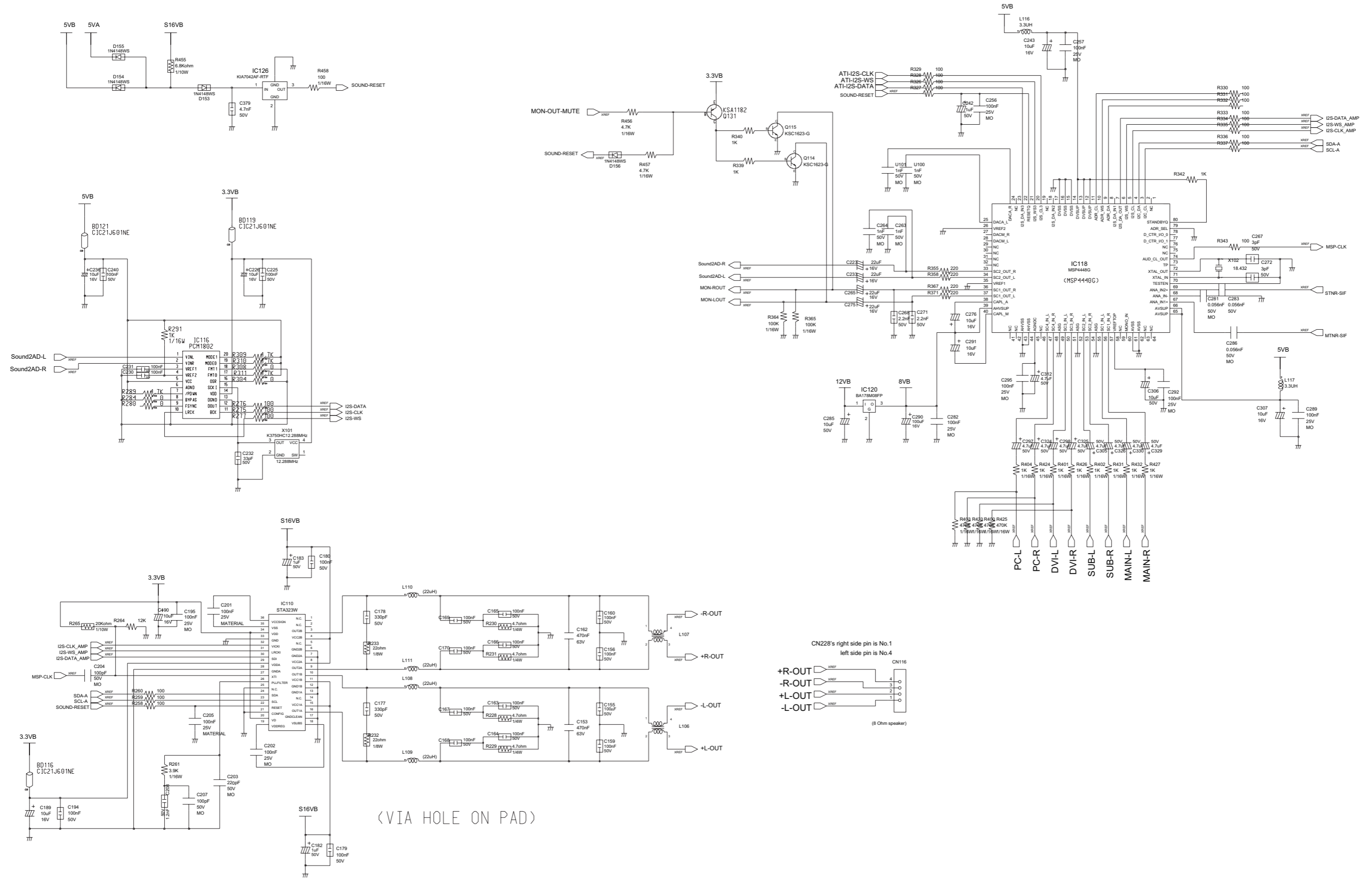
10-1-5 AV\_Comp\_Buffer



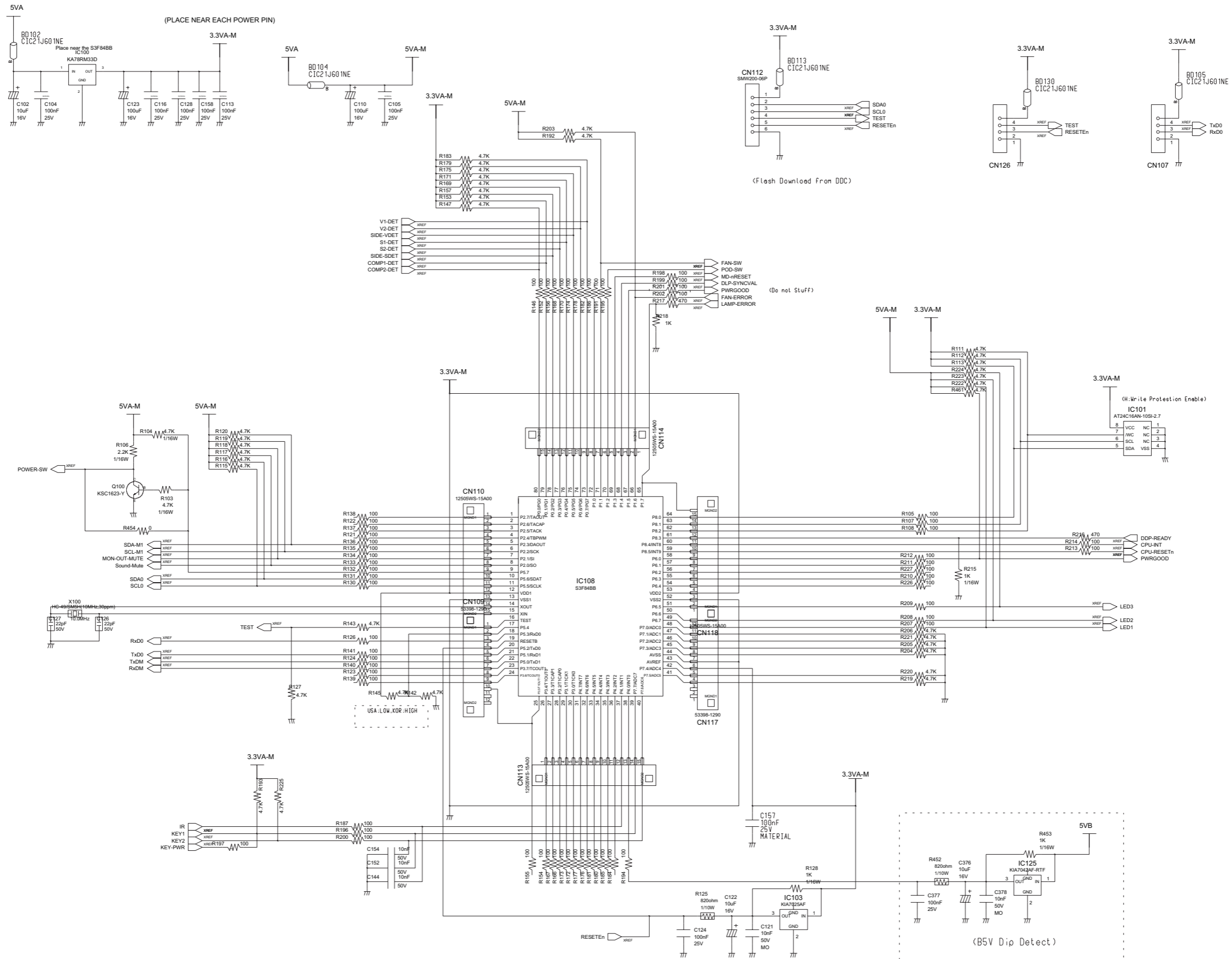
10-1-6 3D-Comb



# 10-1-7 Sound Processor/Amp

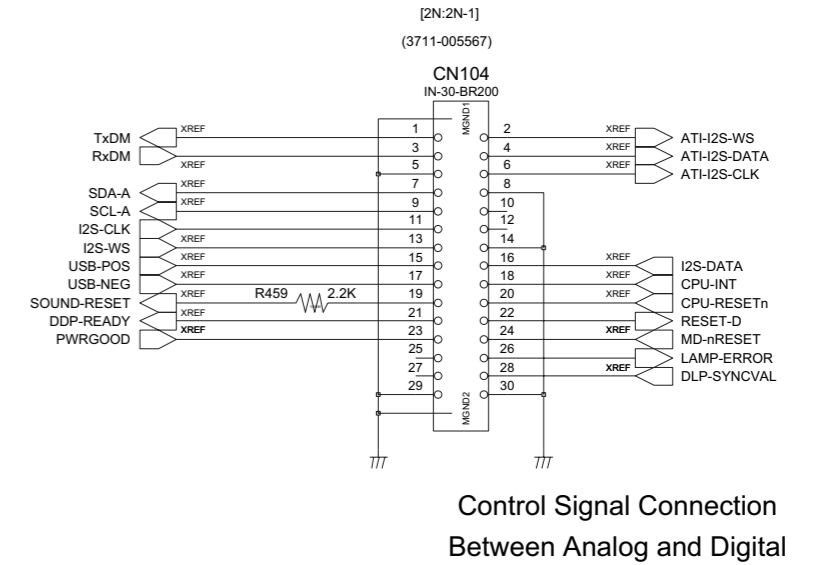
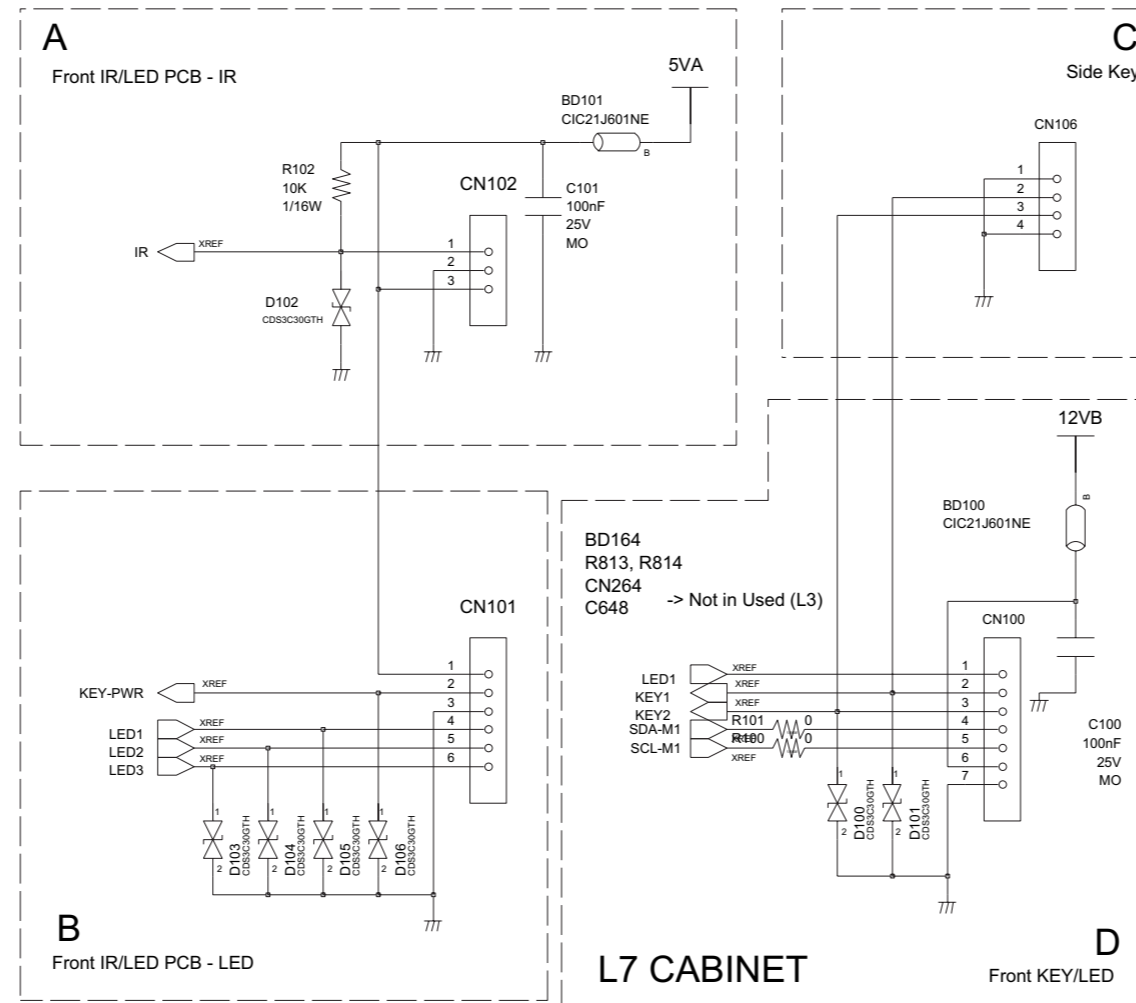
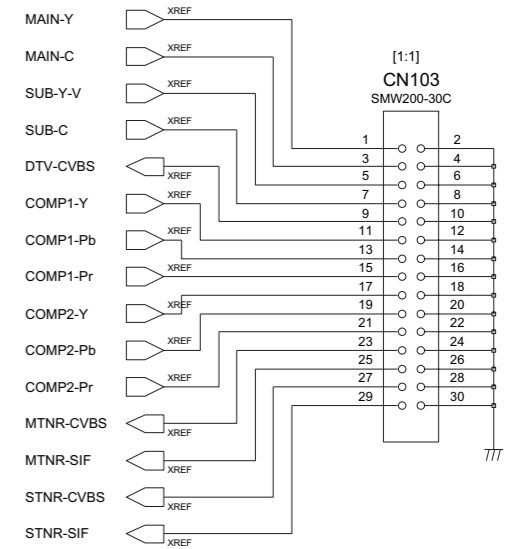
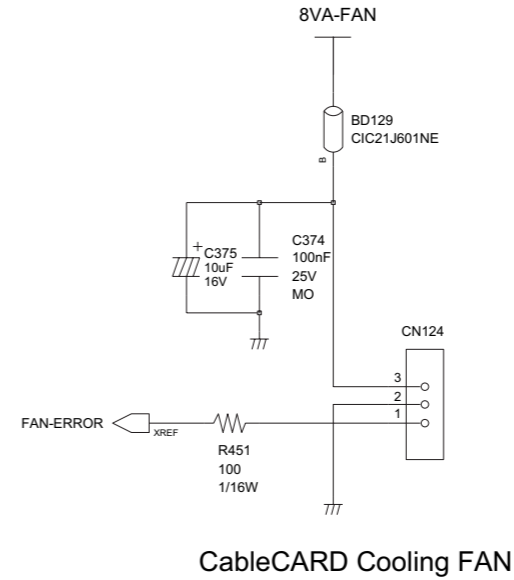
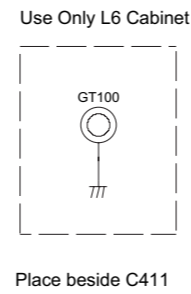
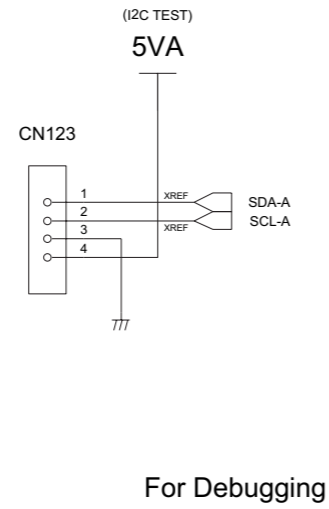
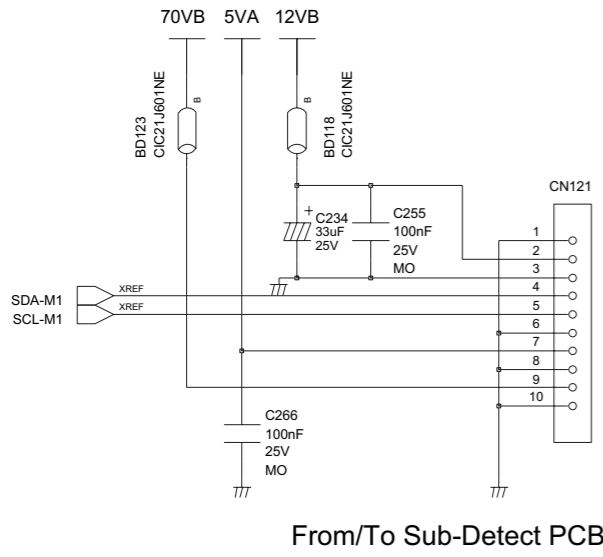


10-1-8 S3F84BB\_Micom





10-1-9 Signal Connection

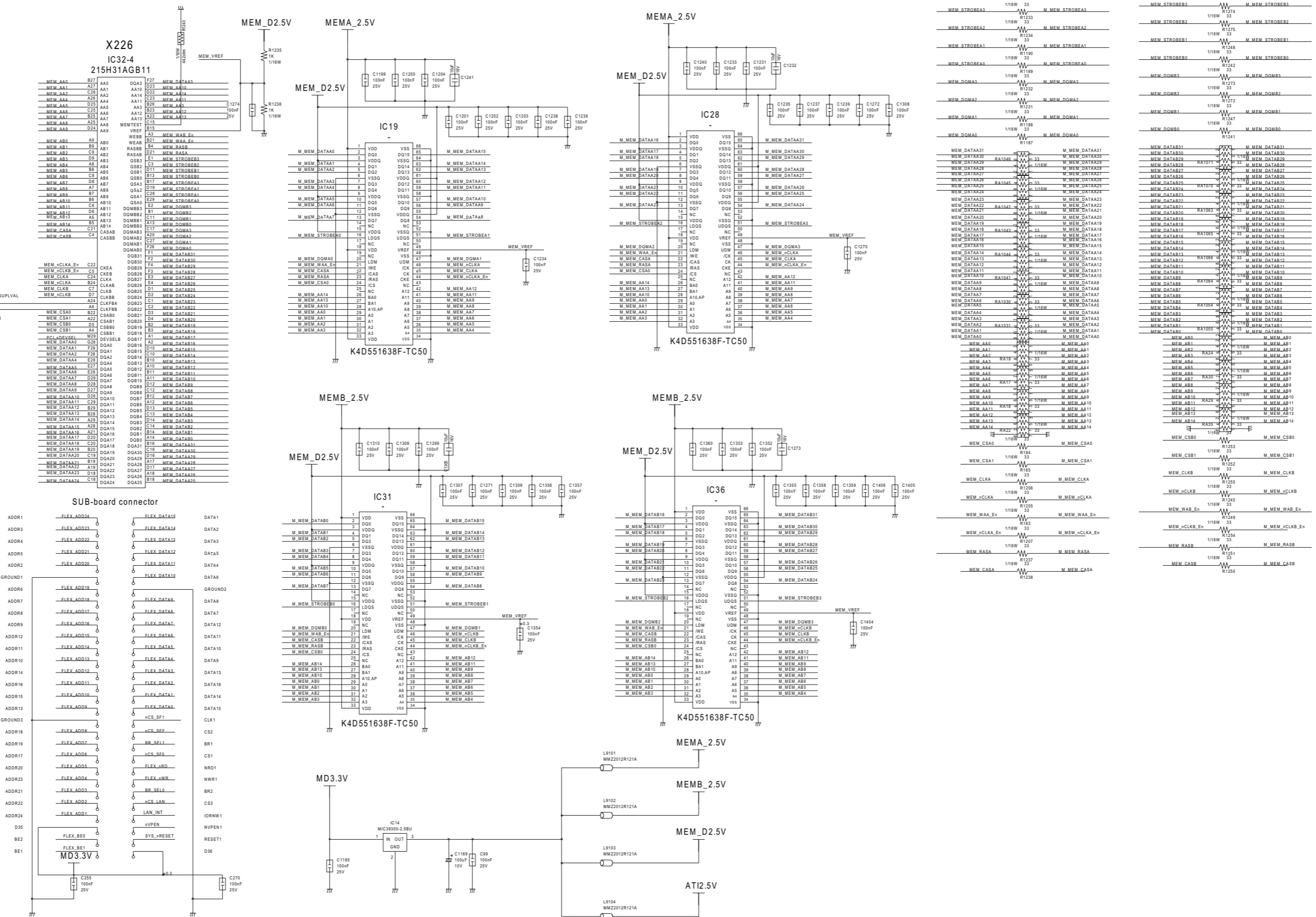


A	B	C	D
1. IR	1. 5VA	1. GND	1. LED1
2. GND	2. KEY-PWR	2. KEY1	2. KEY1
3. 5VA	3. GND	3. KEY2	3. KEY2
	4. LED1	4. GND	4. SDA-M1
	5. LED2		5. SCL-M1
	6. LED3		6. 5VB
			7. GND

Used as follows...  
L3 : A+B, C  
L5 : A, B, C  
L7 : A, D

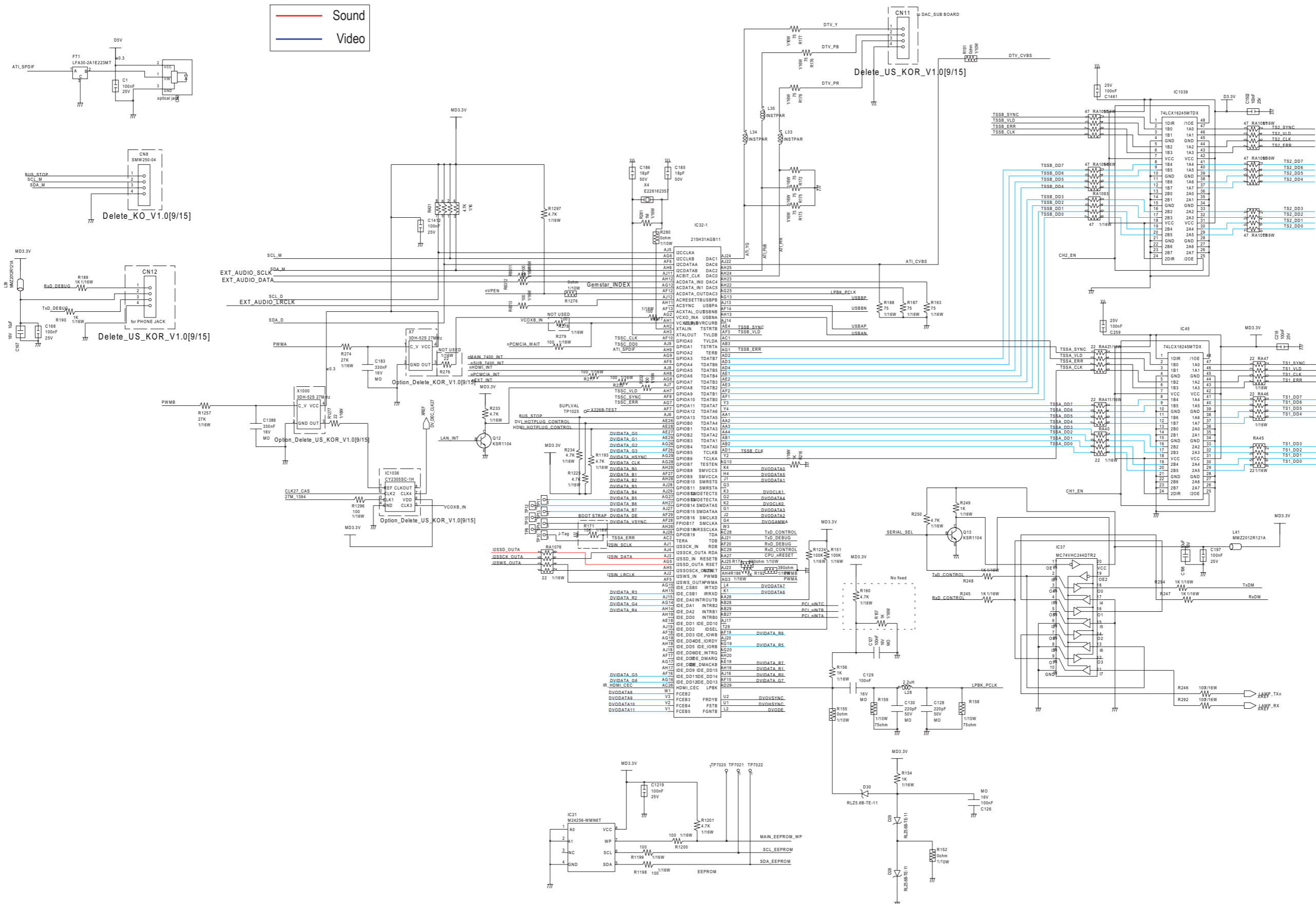
# 10-2 Digital Board

## 10-2-1 DTV Module-1

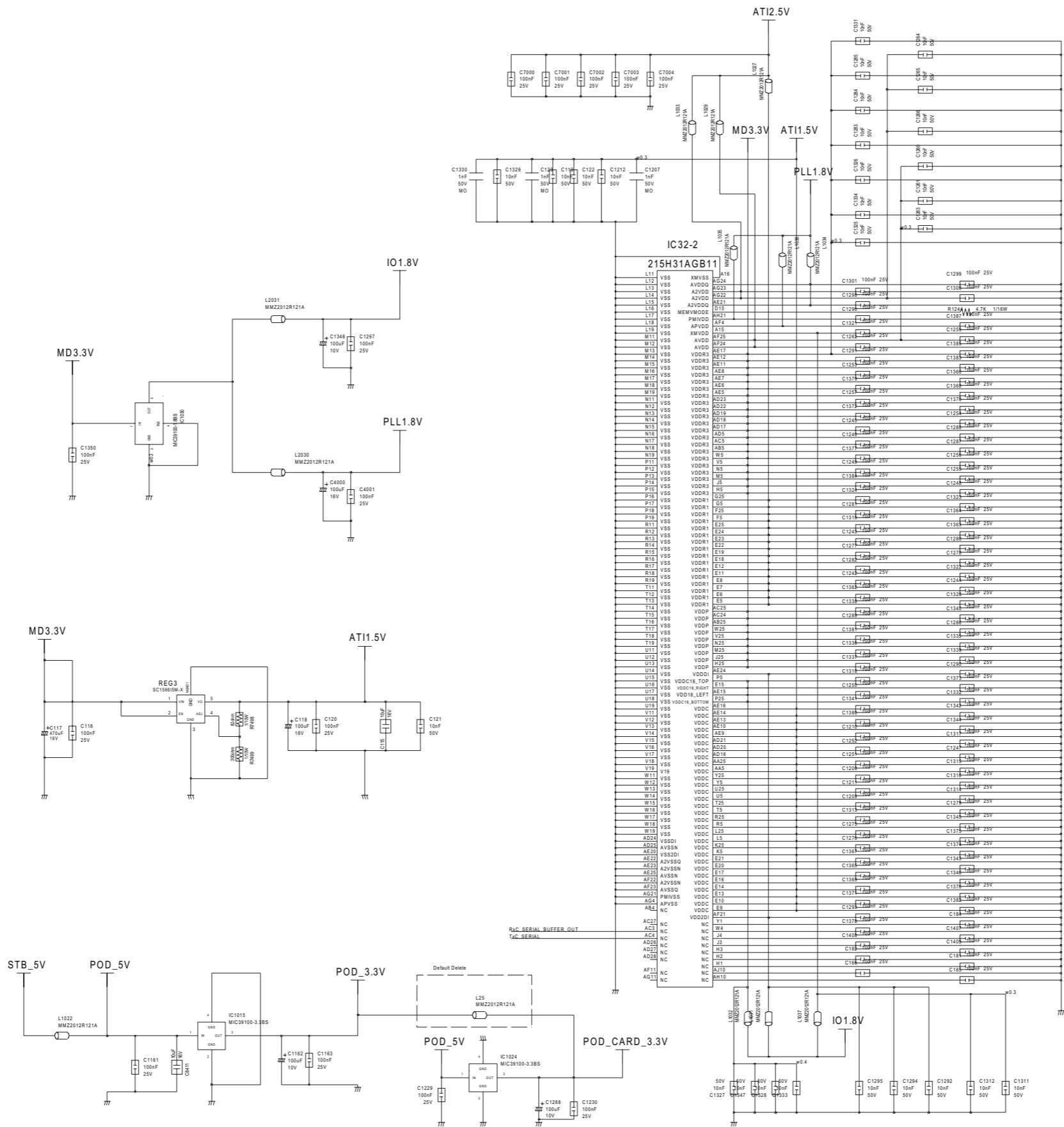




10-2-3 DTV Module-3

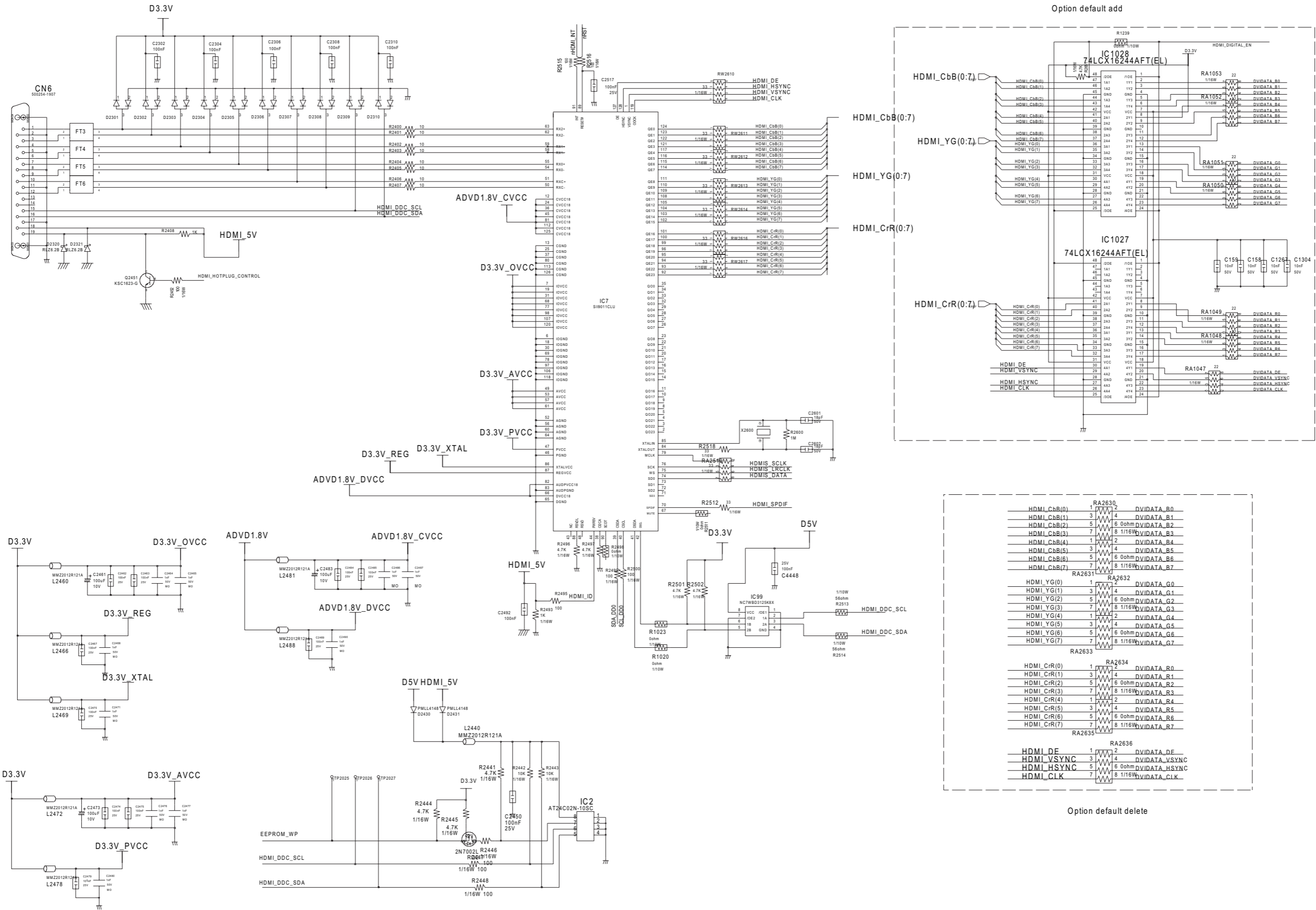


10-2-4 DTV Module-4



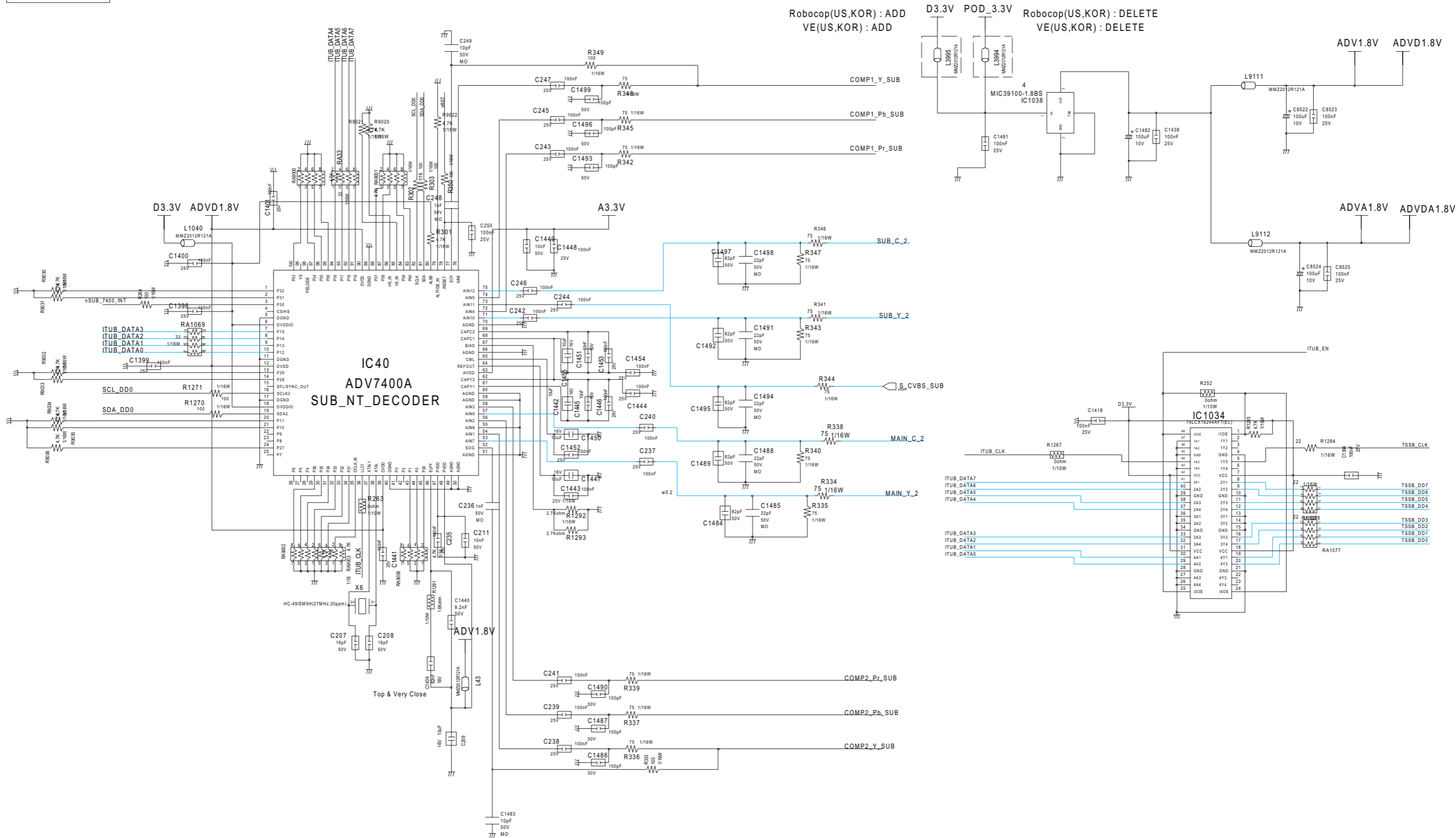


10-2-6 DTV Module-6



HDMI_CbB(0)	RA2630	DVIDATA_B0
HDMI_CbB(1)	3	DVIDATA_B1
HDMI_CbB(2)	5	6 0ohm DVIDATA_B2
HDMI_CbB(3)	7	8 1/16W DVIDATA_B3
HDMI_CbB(4)	1	2 DVIDATA_B4
HDMI_CbB(5)	3	4 DVIDATA_B5
HDMI_CbB(6)	5	6 0ohm DVIDATA_B6
HDMI_CbB(7)	7	8 1/16W DVIDATA_B7
RA2631		
HDMI_YG(0)	RA2632	DVIDATA_G0
HDMI_YG(1)	3	4 DVIDATA_G1
HDMI_YG(2)	5	6 0ohm DVIDATA_G2
HDMI_YG(3)	7	8 1/16W DVIDATA_G3
HDMI_YG(4)	1	2 DVIDATA_G4
HDMI_YG(5)	3	4 DVIDATA_G5
HDMI_YG(6)	5	6 0ohm DVIDATA_G6
HDMI_YG(7)	7	8 1/16W DVIDATA_G7
RA2633		
HDMI_Cr(0)	RA2634	DVIDATA_R0
HDMI_Cr(1)	3	4 DVIDATA_R1
HDMI_Cr(2)	5	6 0ohm DVIDATA_R2
HDMI_Cr(3)	7	8 1/16W DVIDATA_R3
HDMI_Cr(4)	1	2 DVIDATA_R4
HDMI_Cr(5)	3	4 DVIDATA_R5
HDMI_Cr(6)	5	6 0ohm DVIDATA_R6
HDMI_Cr(7)	7	8 1/16W DVIDATA_R7
RA2635		
RA2636		
HDMI_DE	1	2 DVIDATA_DE
HDMI_VSYNC	3	4 DVIDATA_VSYNC
HDMI_HSYNC	5	6 0ohm DVIDATA_HSYNC
HDMI_CLK	7	8 1/16W DVIDATA_CLK

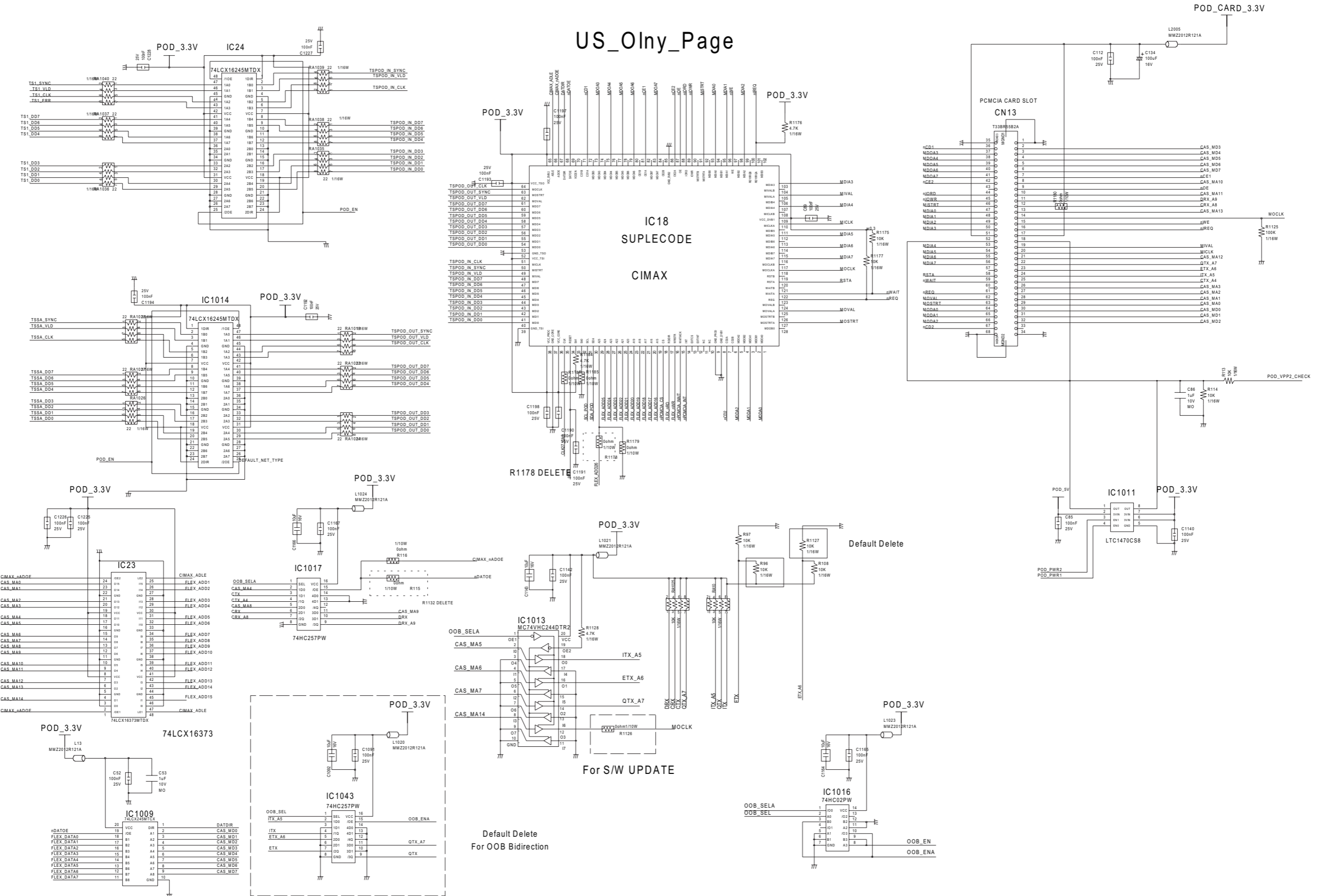
10-2-7 DTV Module-7



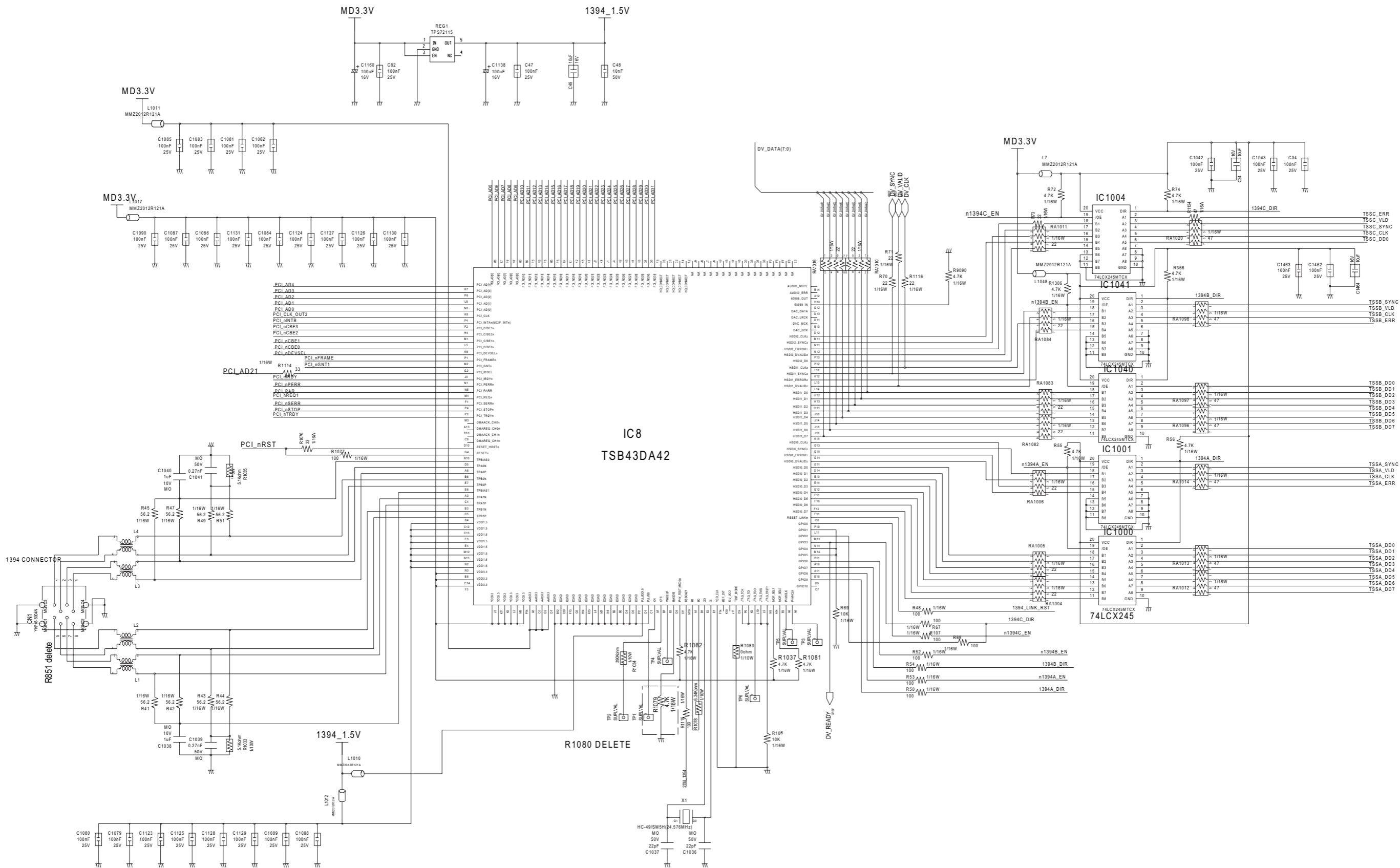


10-2-8 DTV Module-8

US\_OIny\_Page



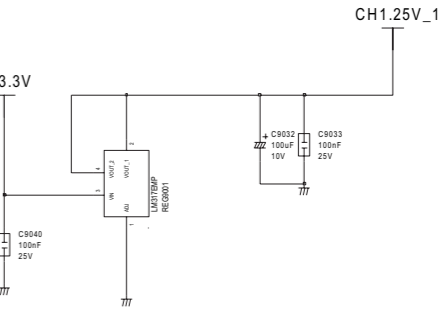
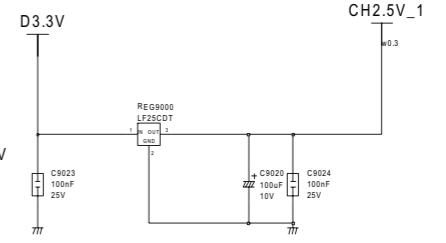
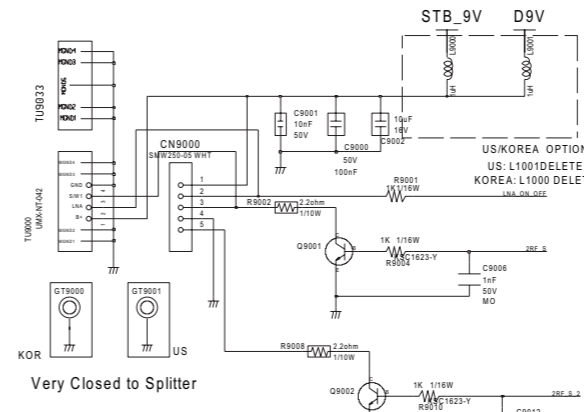
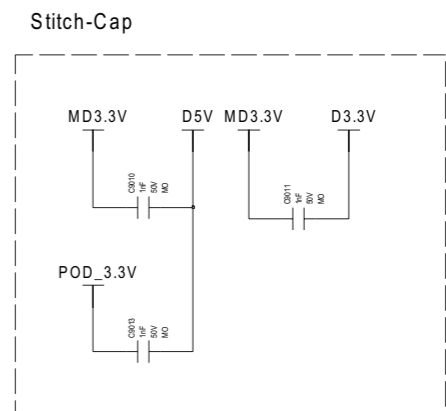
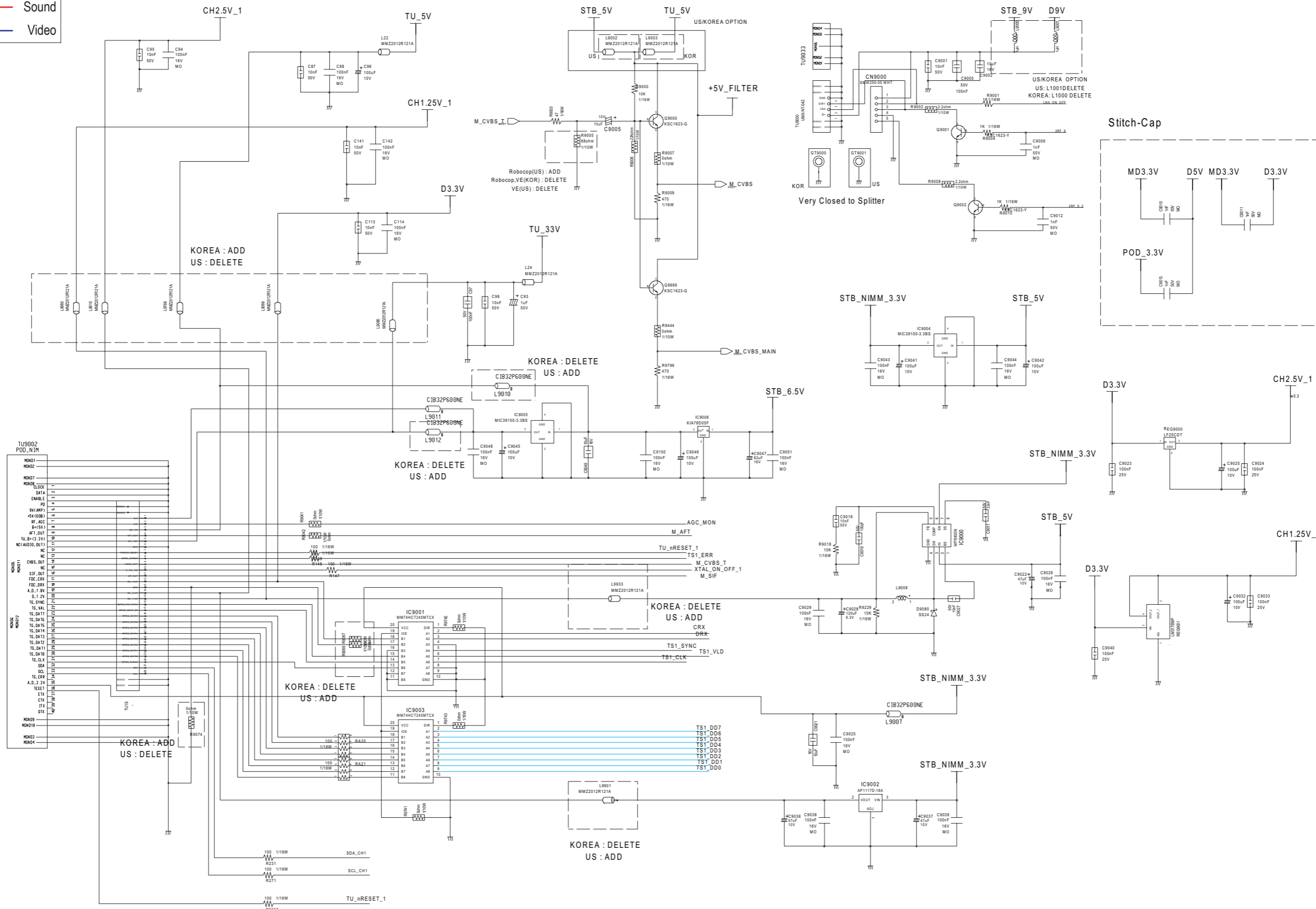
10-2-9 DTV Module-9



# 10-2-10 DTV Module-10

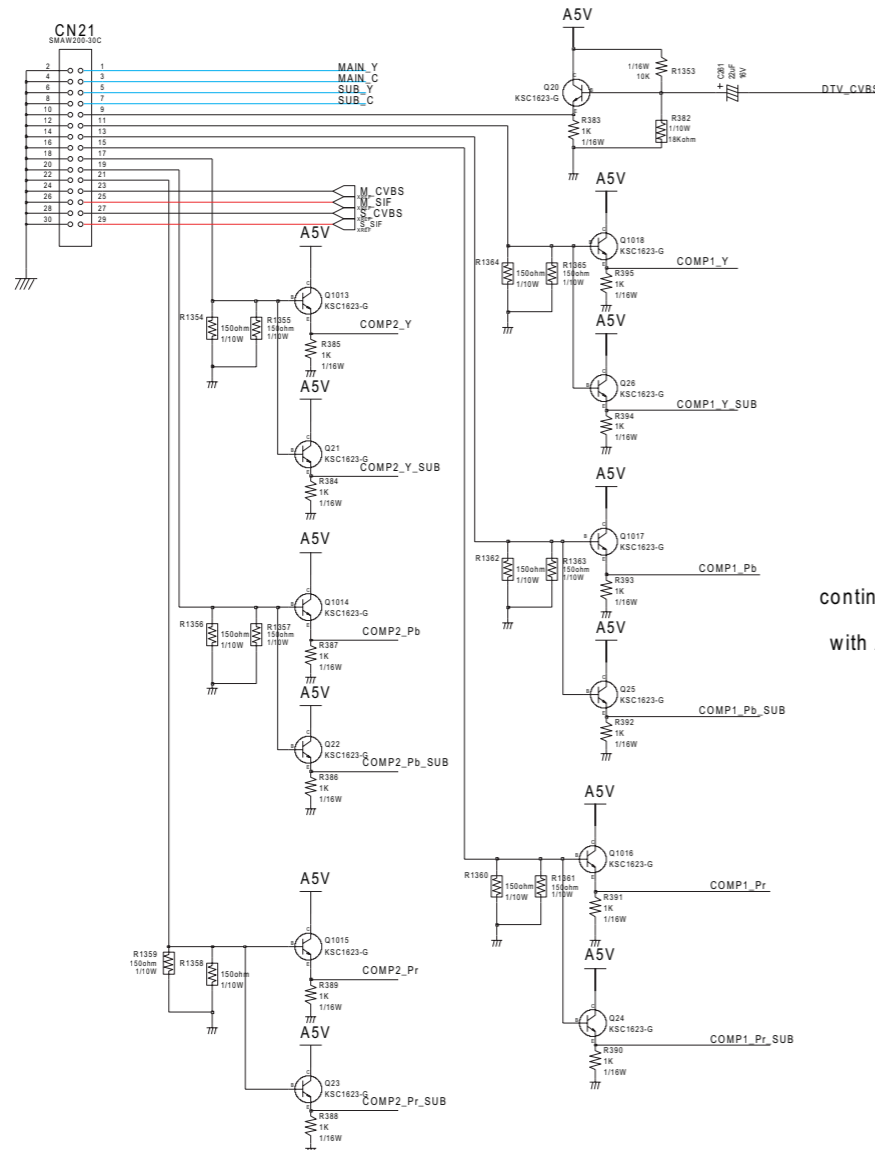
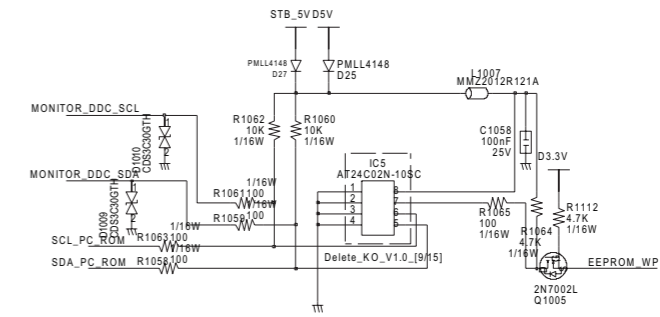
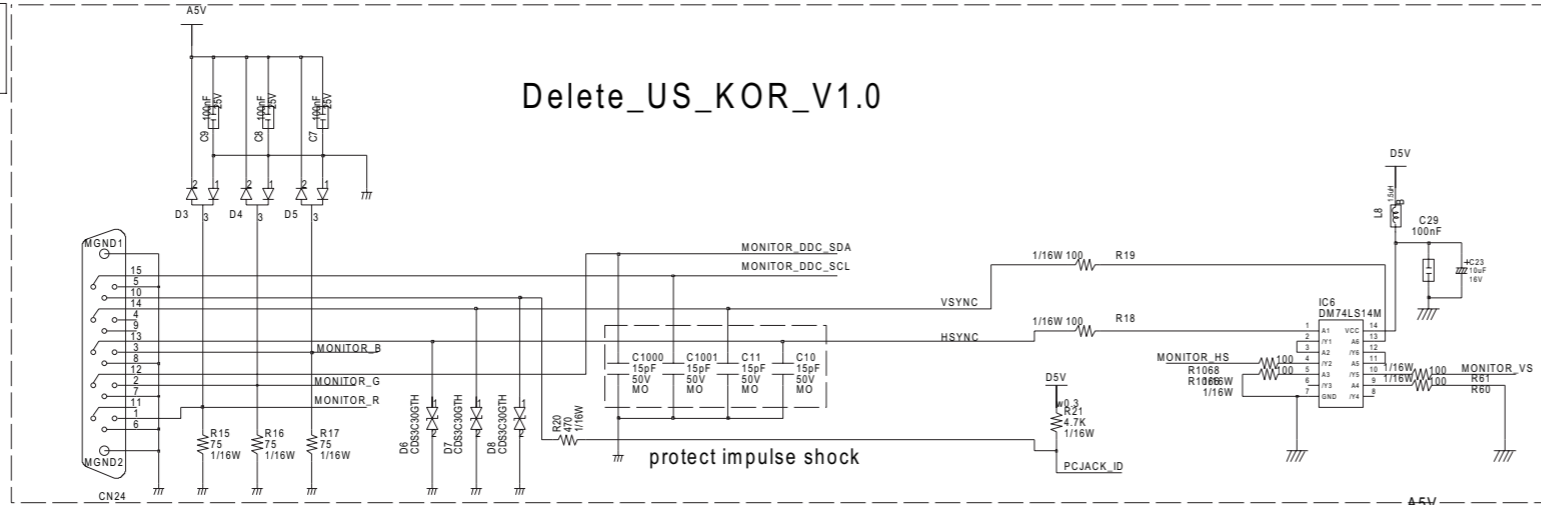
Legend:

- Sound (Red line)
- Video (Blue line)

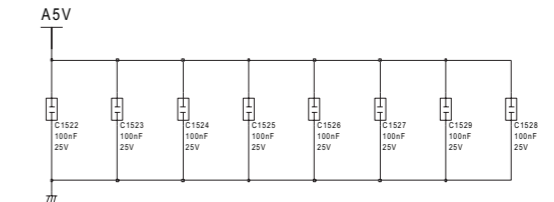
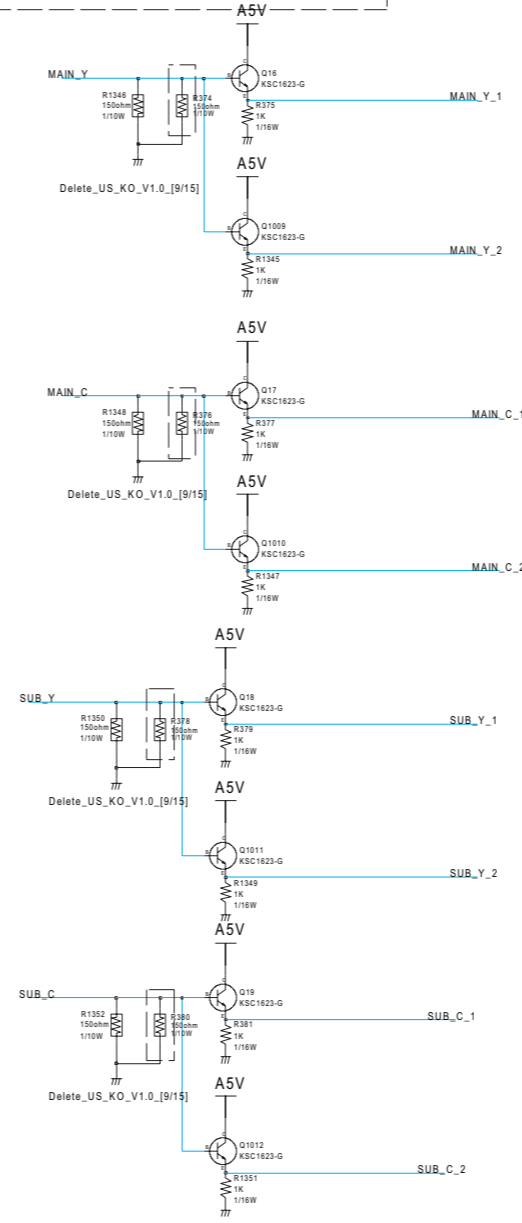




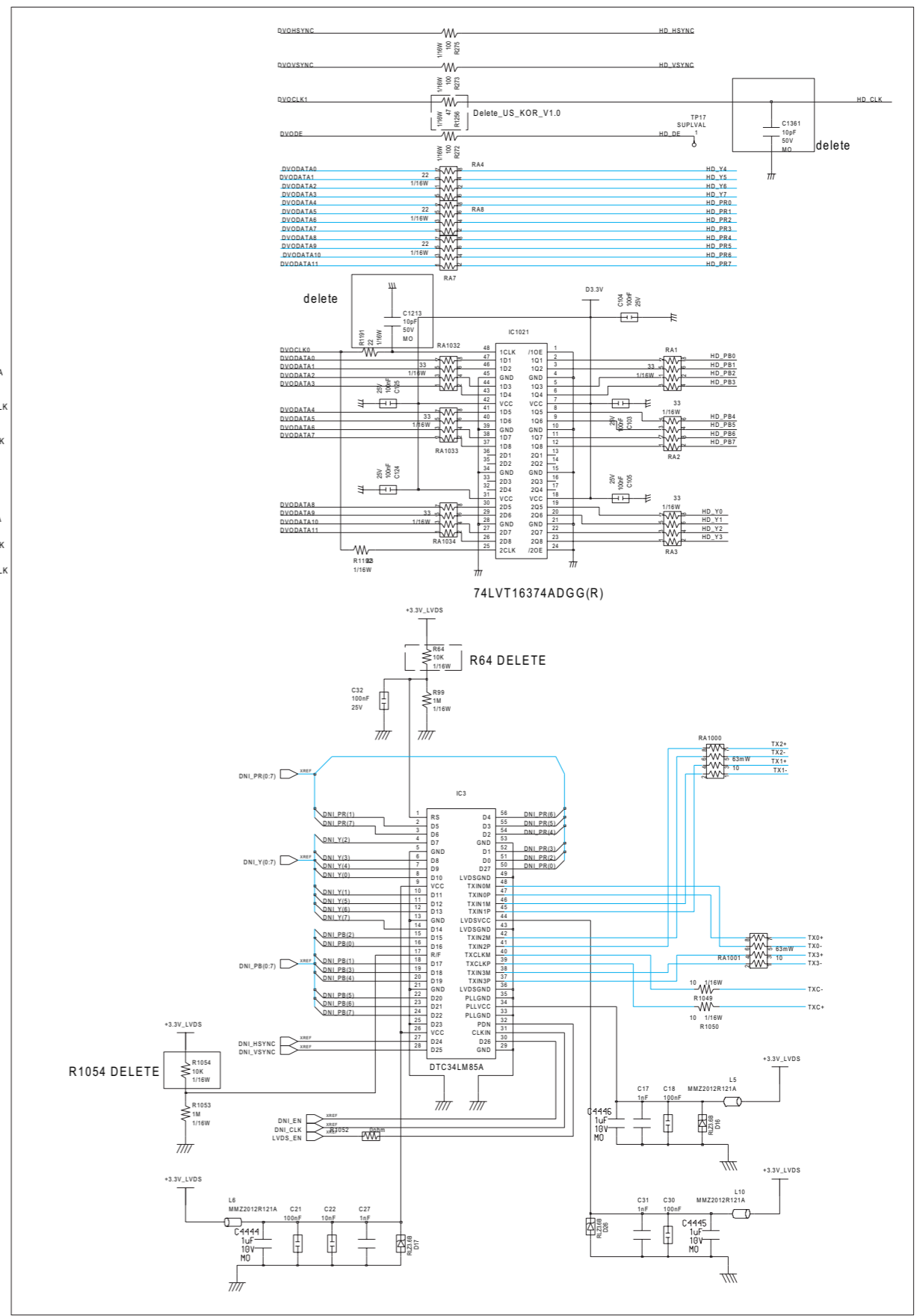
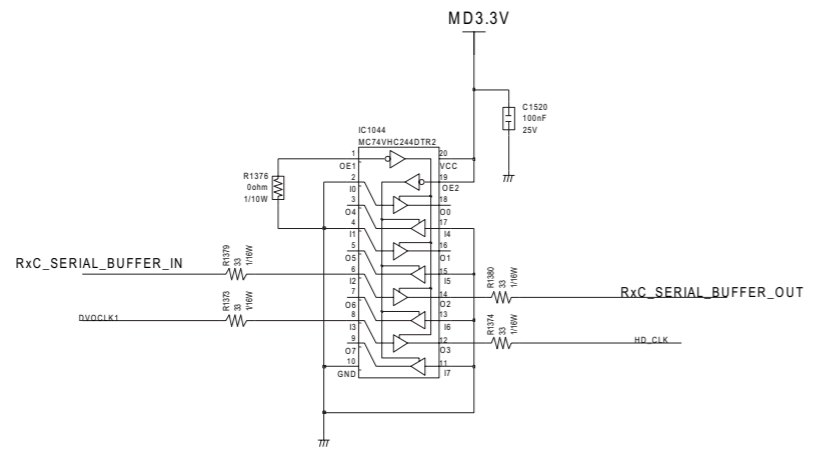
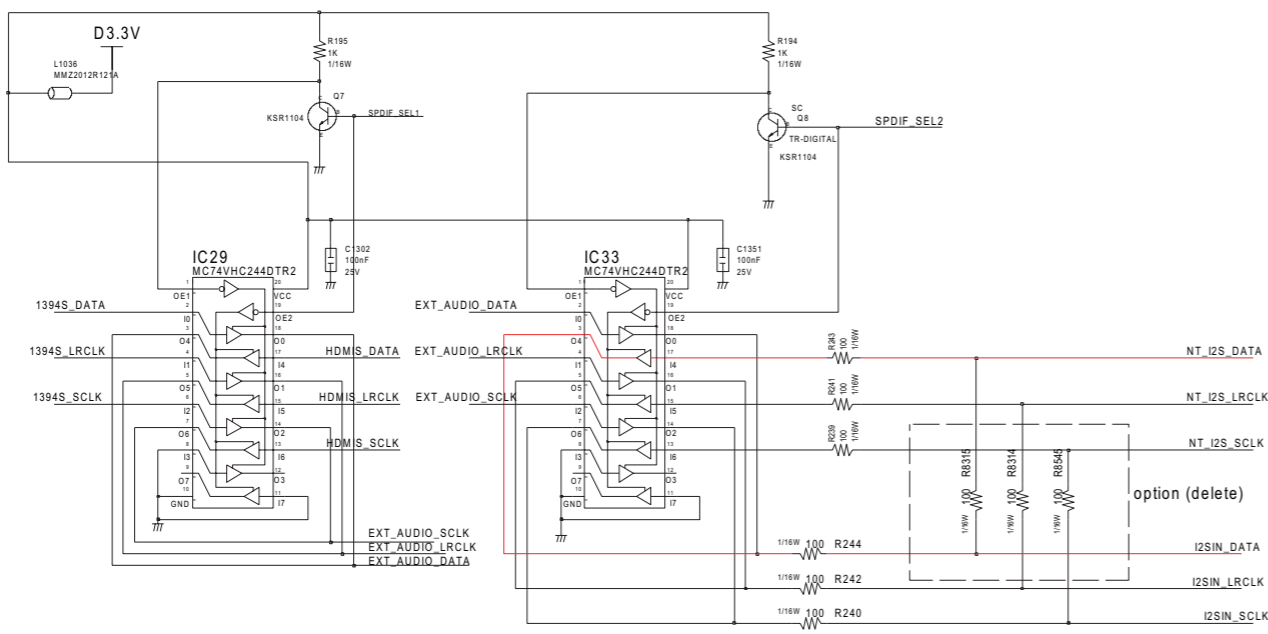
10-2-12 DTV Module-12



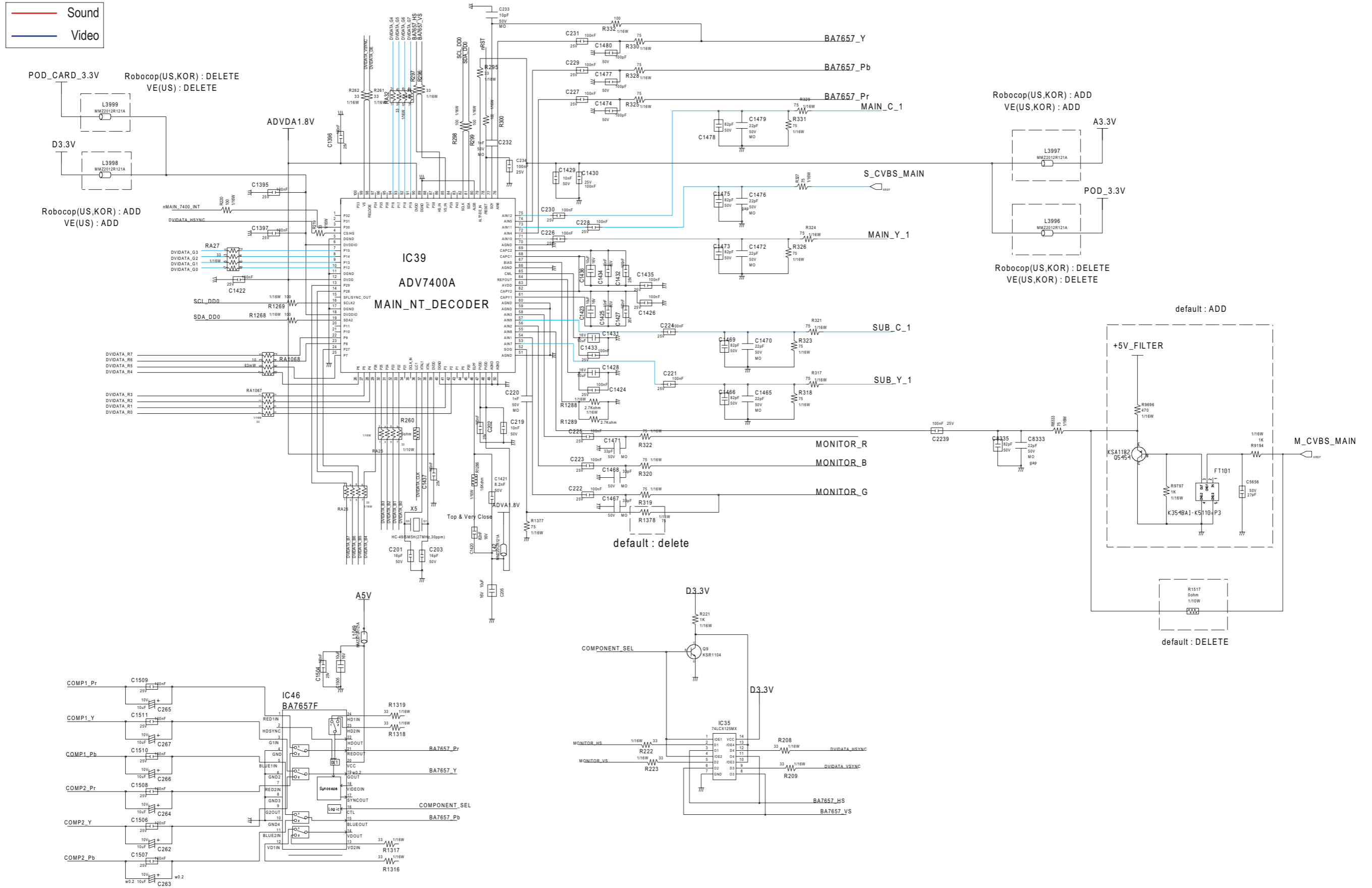
continue # 14 - 13  
with ADV7402



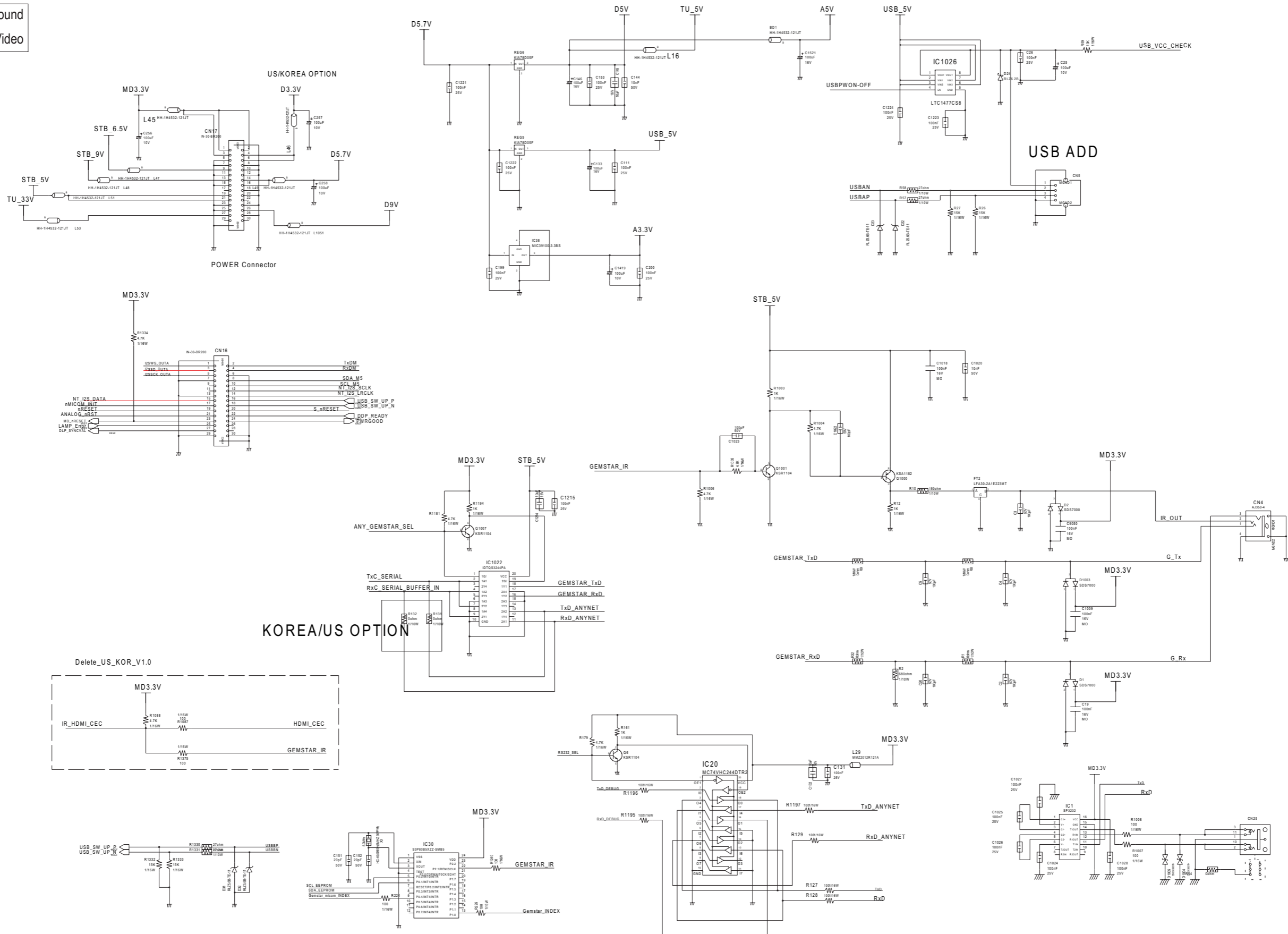
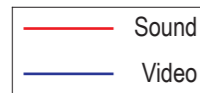
### 10-2-13 DTV Module-13



10-2-14 DTV Module-14



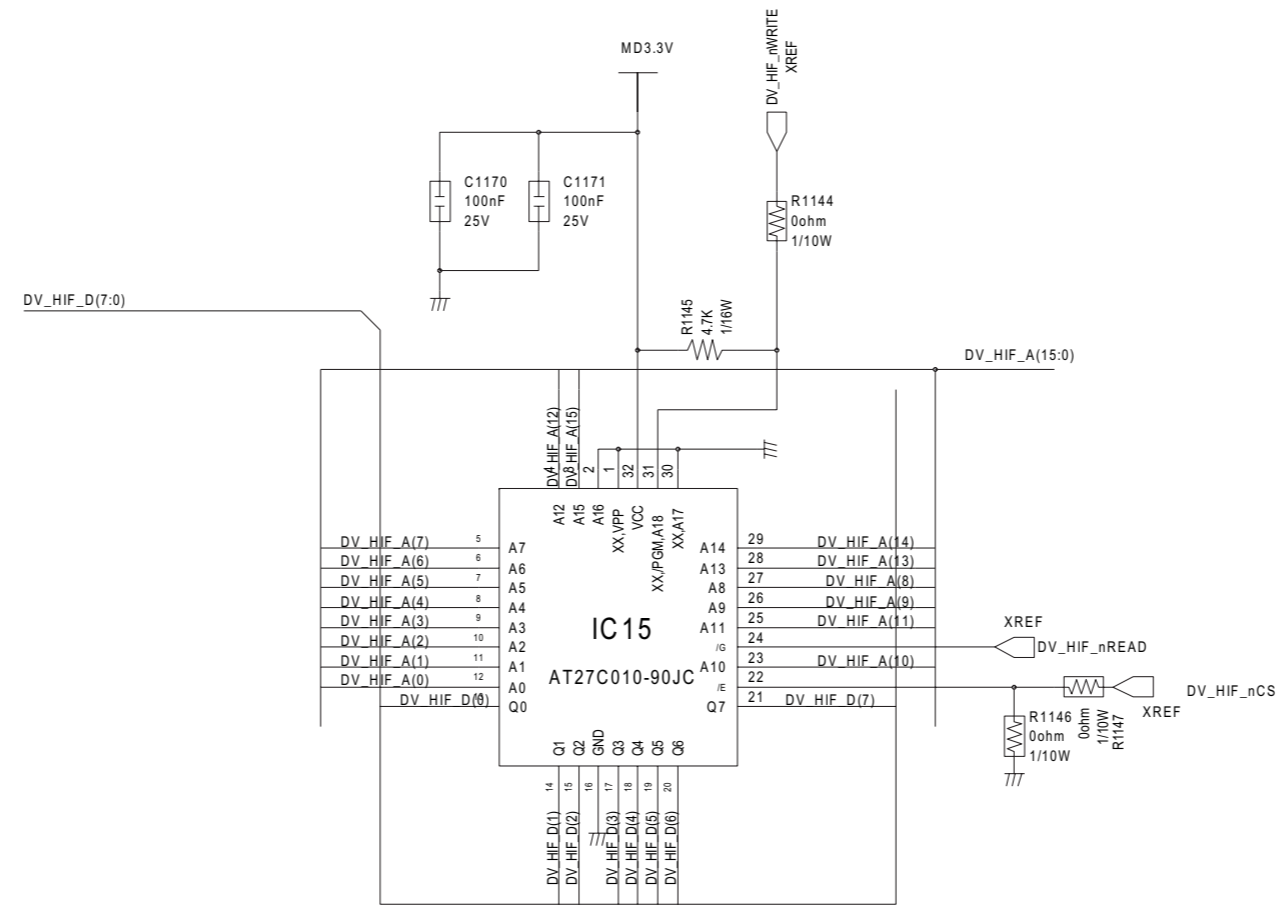
10-2-15 DTV Module-15







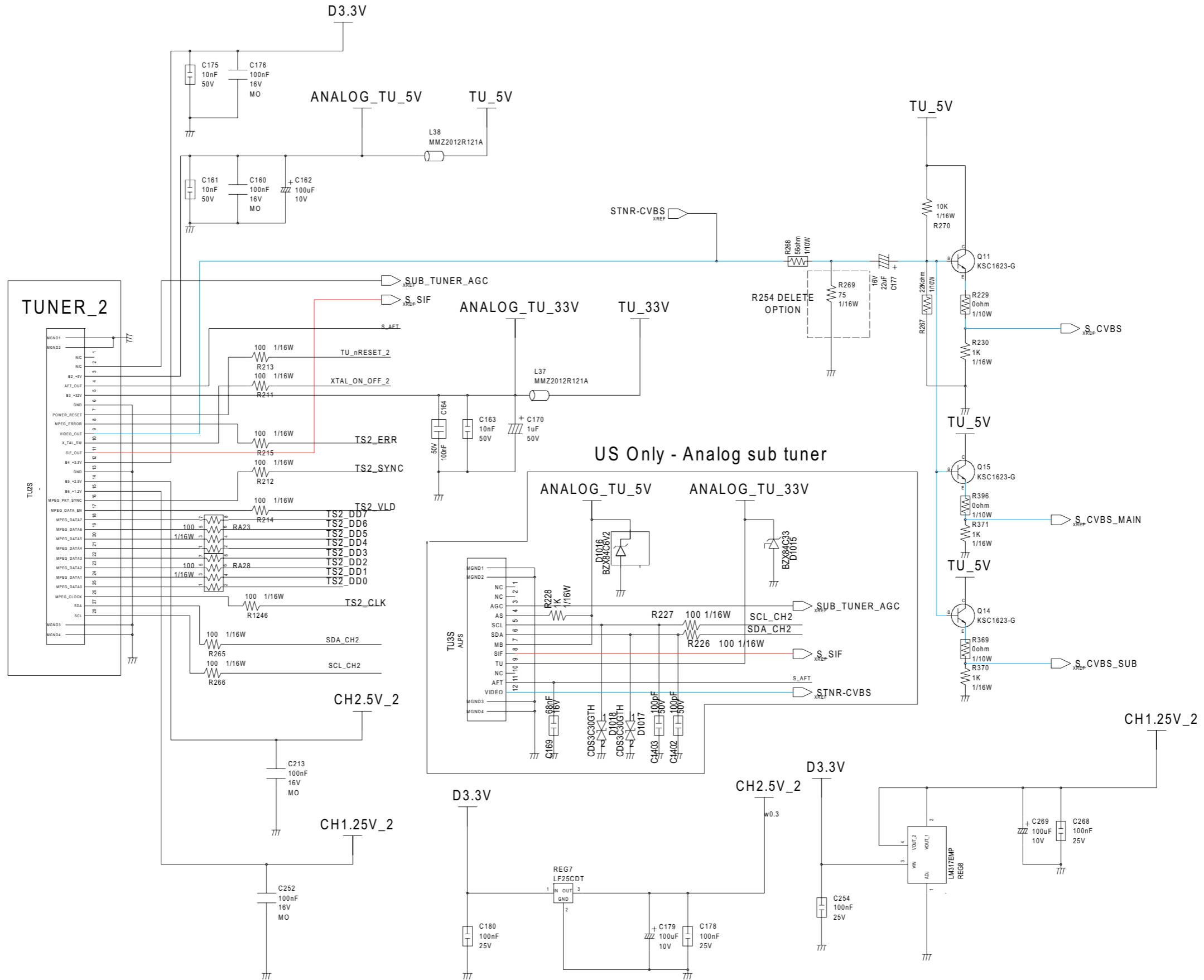
10-2-17 DTV Module-17



10-2-18 DTV Module-18



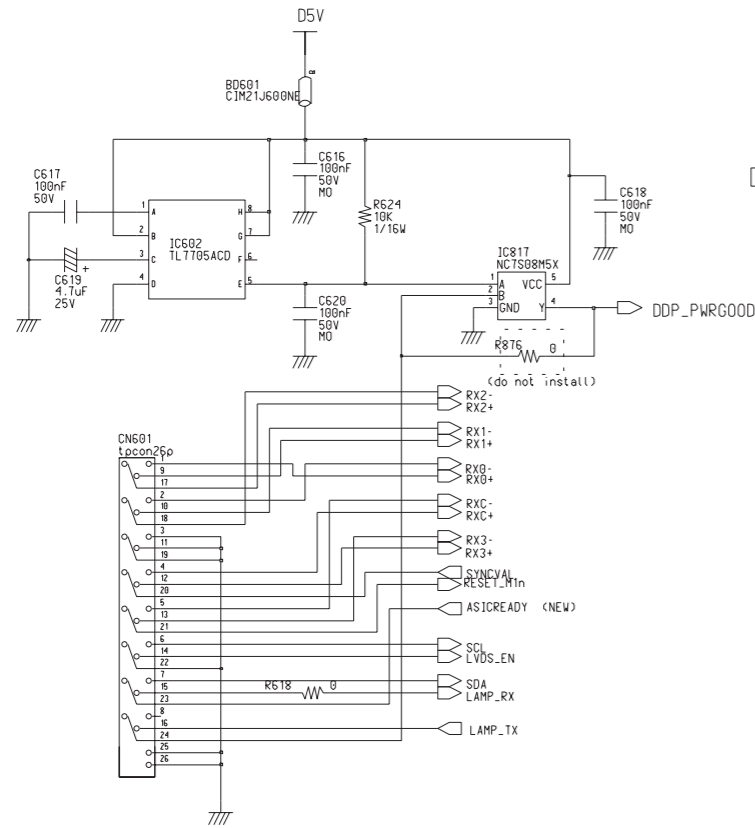
KO Olmy - Sub alps NIM tuner



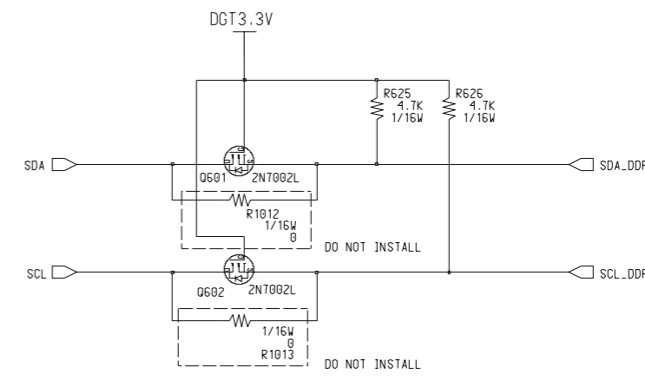
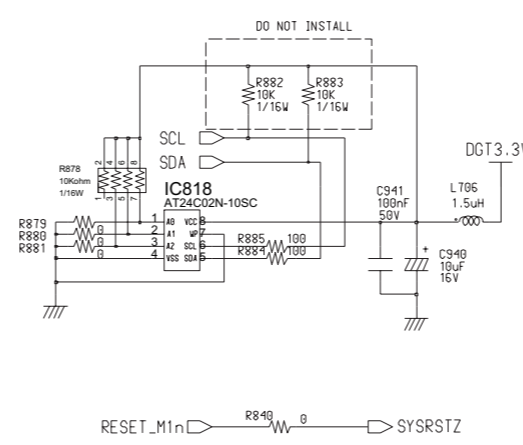
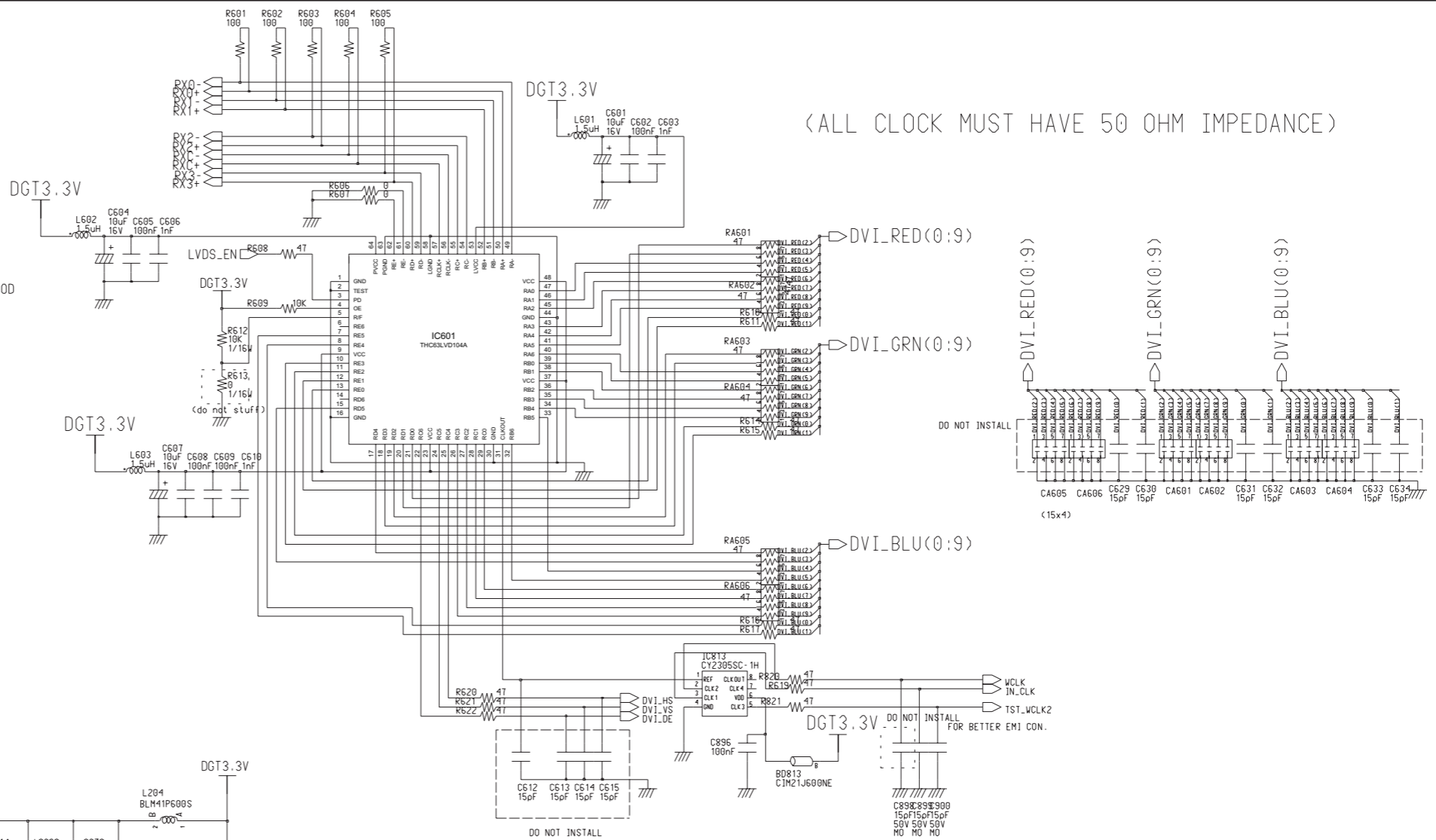
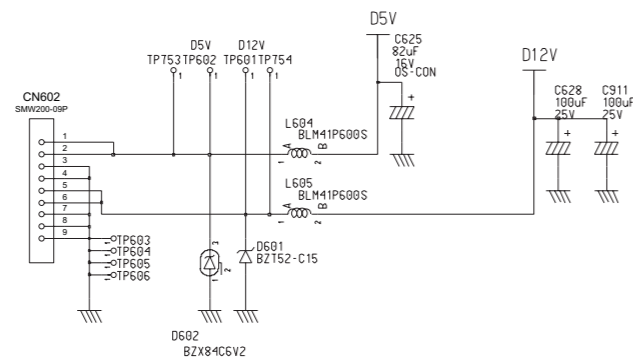
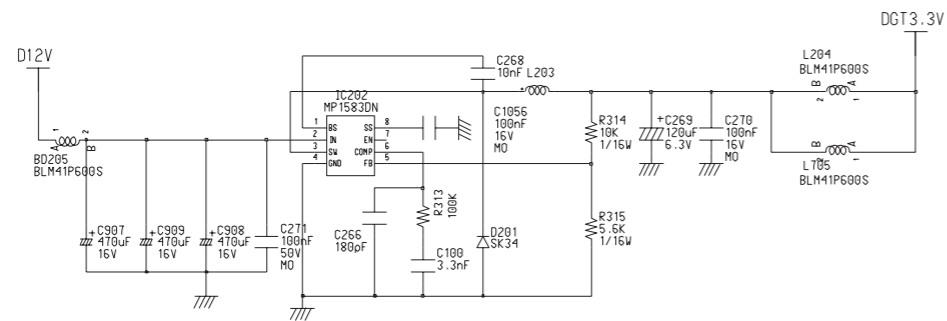
# 10-3 DMD Board

## 10-3-1 DMD Board-1

( DDP1011 CONTROL OPTION FROM PC )

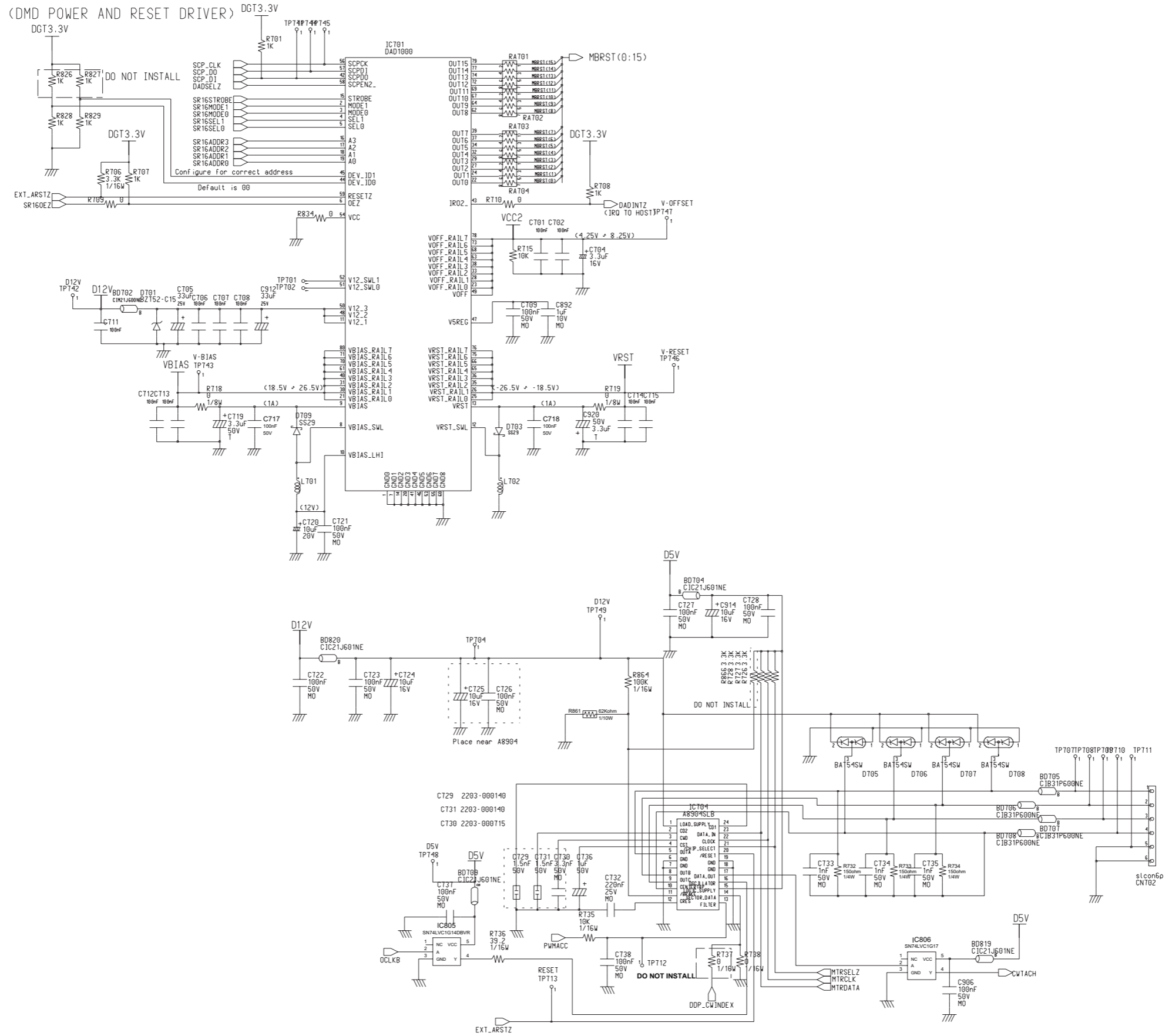


(DVI-D STRAIGHT )  
(CONNECT GUARD PINS TO GROUND)

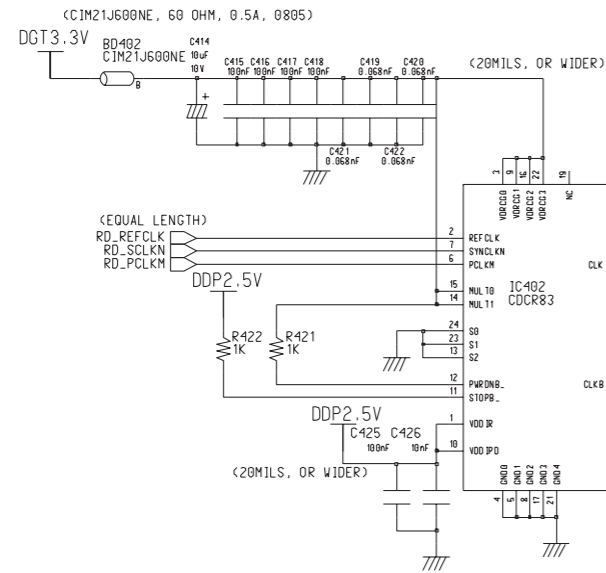
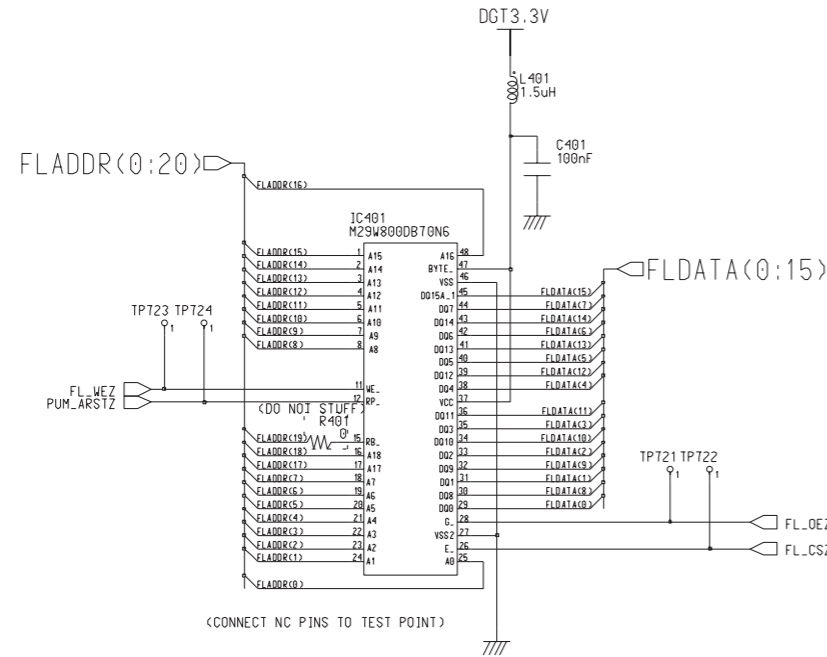




10-3-3 DMD Board-3

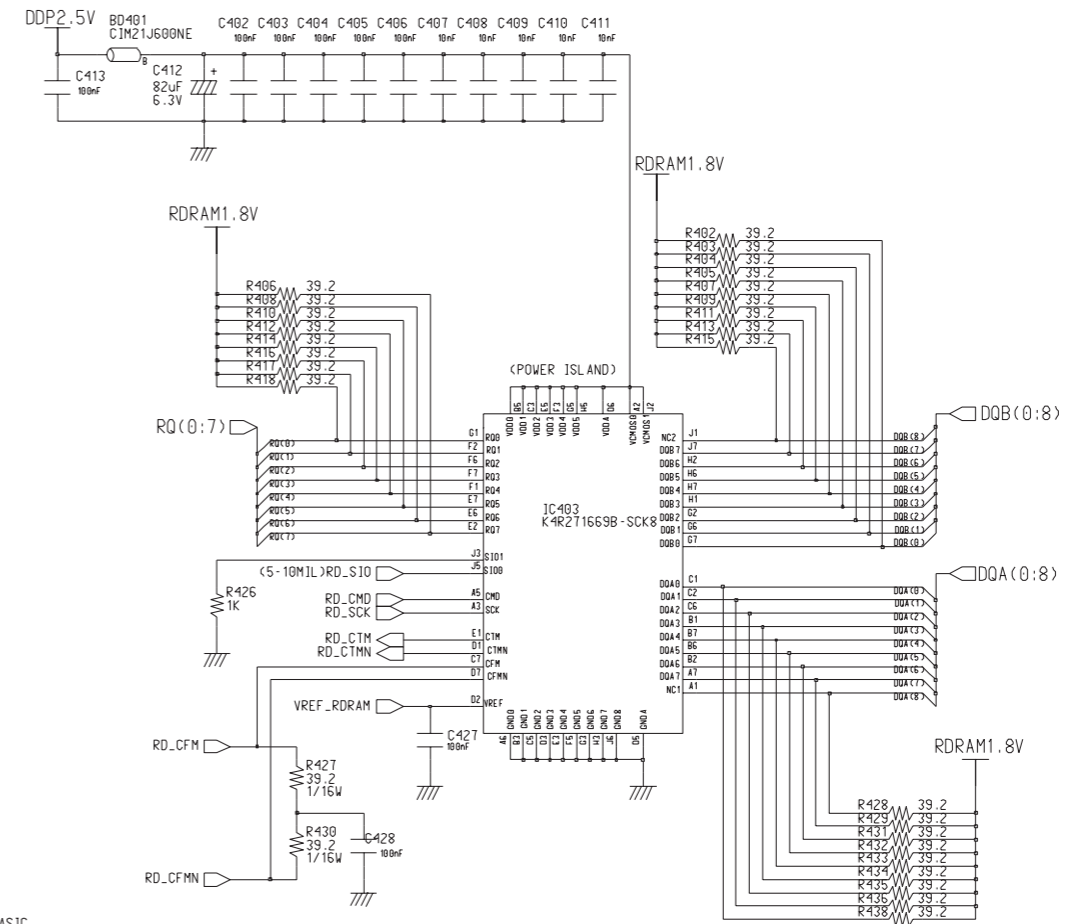


10-3-4 DMD Board-4

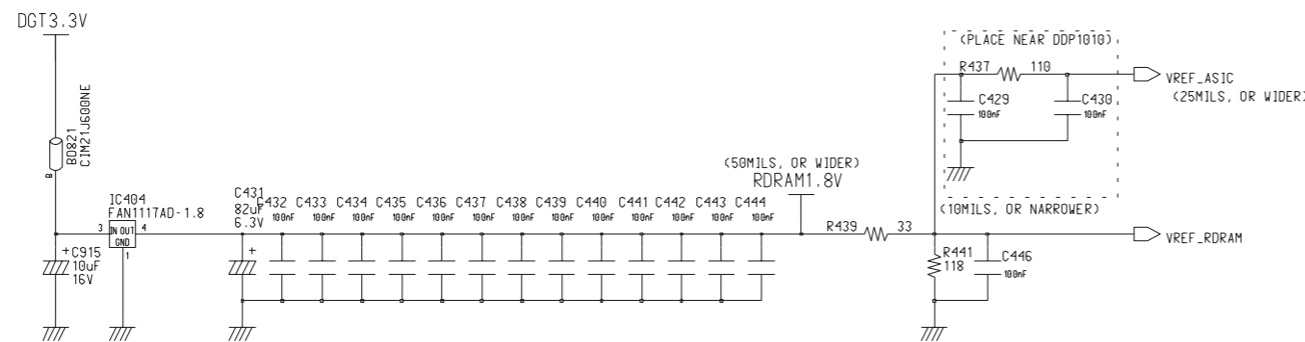


<PAIR, IMPEDANCE: UNLOADED :40 OHM, LOADED:60.6 OHM>  
 <ROUTING: CDCR83-> RDRAM(CTM) -> DDP1010(CFM) -> RDRAM -> TERMINATION RESISTOR)>  
 <RSL : RQ(0:7), RQA(0:8), ROB(0:8), RD\_CTM, RD\_CTMN, RD\_CFM, RD\_CFMN, RD\_CMD, RD\_SCK)>  
 <50 OHM IMPEDANCE : RD\_REFCLK, RD\_SCLKN, RD\_PCLKN)>  
 <ALL RSL SIGNALS, SCK, CMD, DIFF CLOCK PAIRS MUST HAVE MATCHED ELECTRICAL LENGTH>

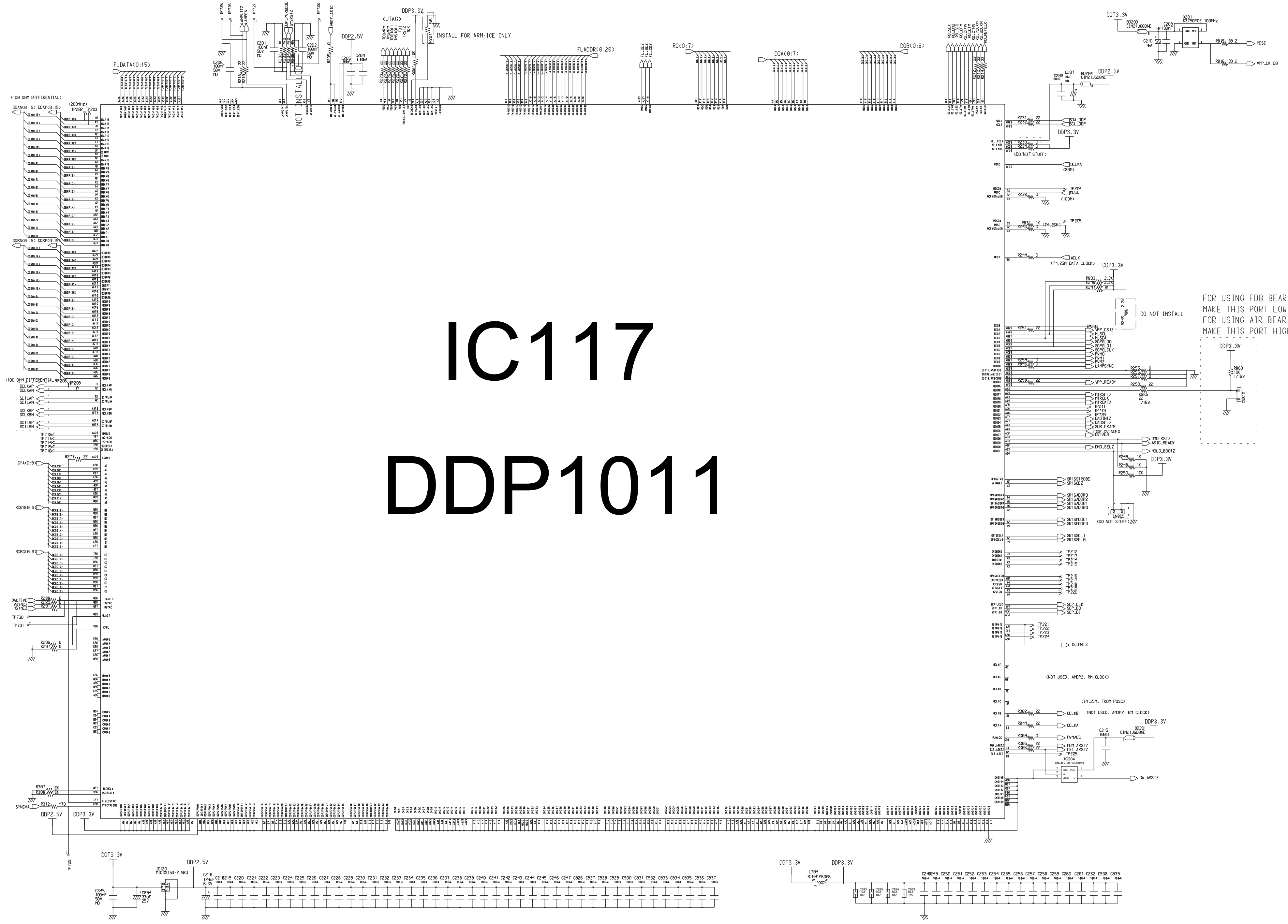
< CHANNEL IMPEDANCE : 40 OHM)>  
 <TRACE THICKNESS : 1 OZ CU)>  
 <LENGTH OF LOADED SEGMENT : 748 MILS)>  
 <TRACE WIDTH FOR UNLOADED SECTION : 10.4 MILS -> DEPENDENT ON VENDOR)>  
 <MINIMUM UNLOADED PITCH : 30 MILS)>  
 <FINISHED DIELECTRIC THICKNESS : 8 MILS -> DEPENDENT ON VENDOR)>



<RDRAM FOR SHORT CHANNEL, 54PIN)>



10-3-5 DMD Board-5



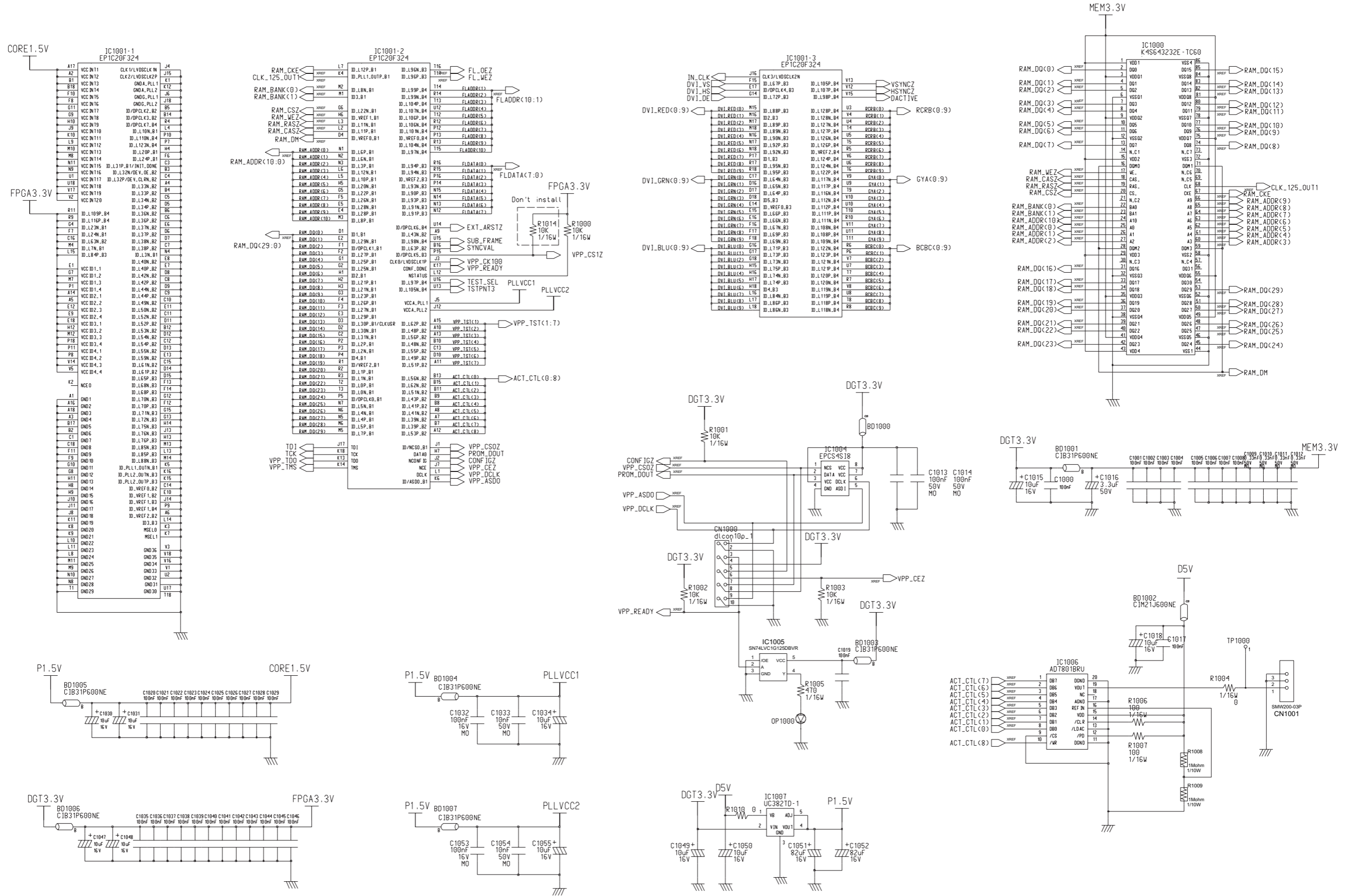
# IC117

# DDP1011

FOR USING FDB BEARING  
MAKE THIS PORT LOW  
FOR USING AIR BEARING  
MAKE THIS PORT HIGH

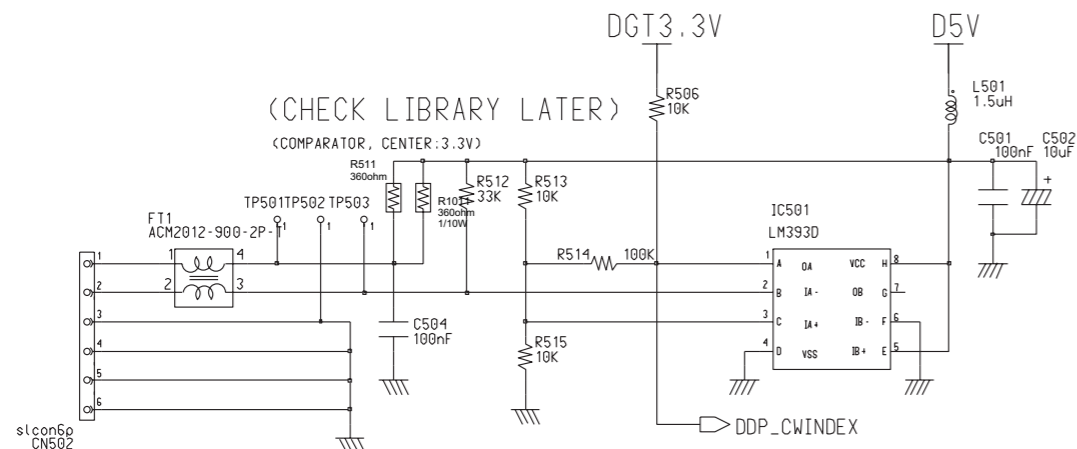


10-3-6 DMD Board-6



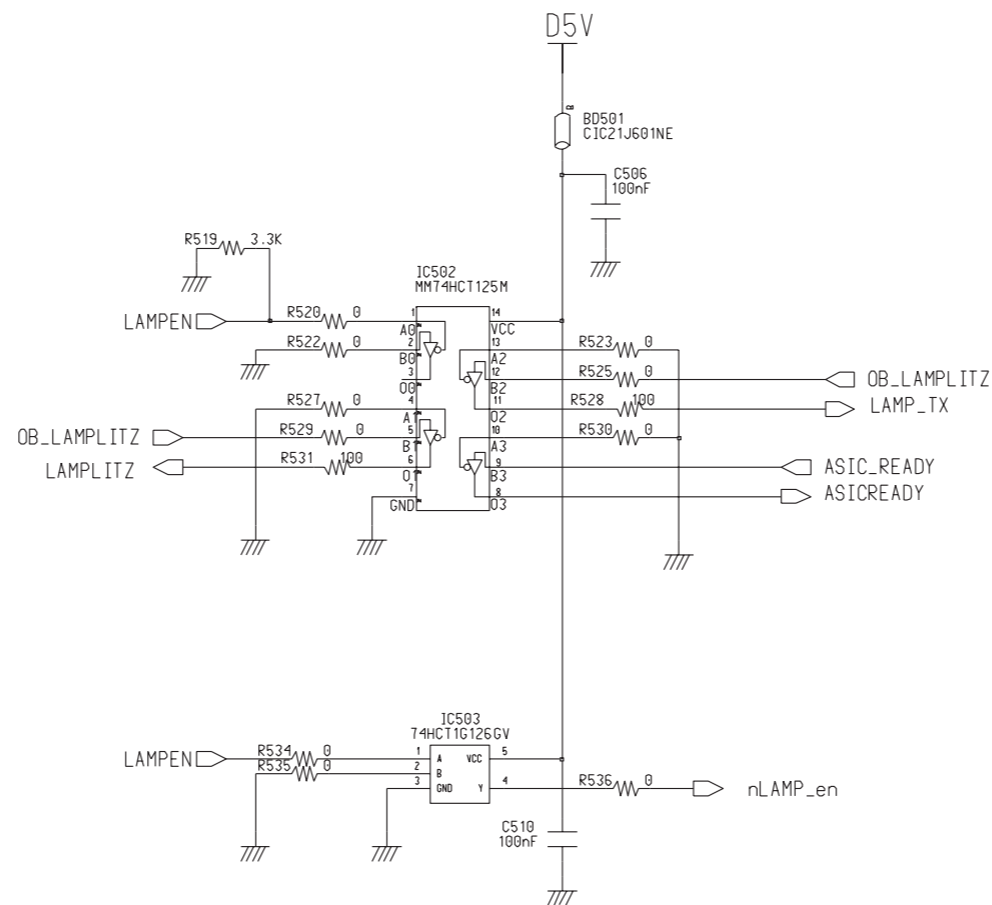
10-3-7 DMD Board-7

(COLOR WHEEL SENSOR DETECTION)

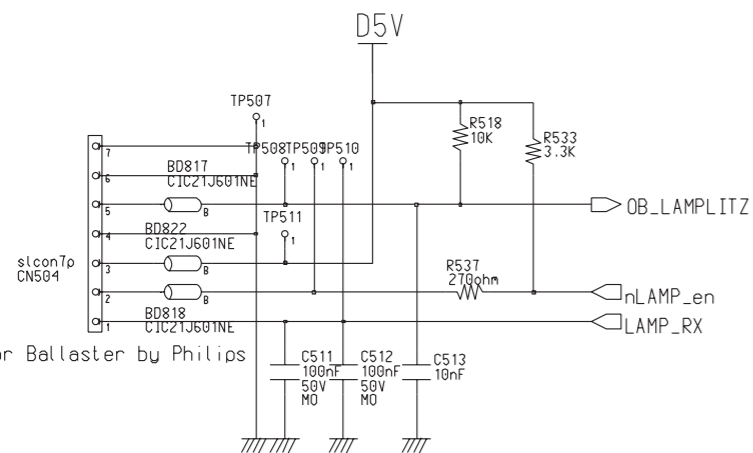


(LAMP CONTROL)

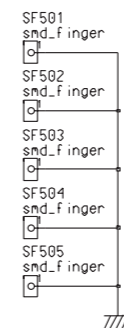
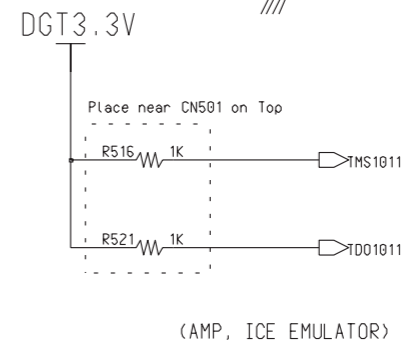
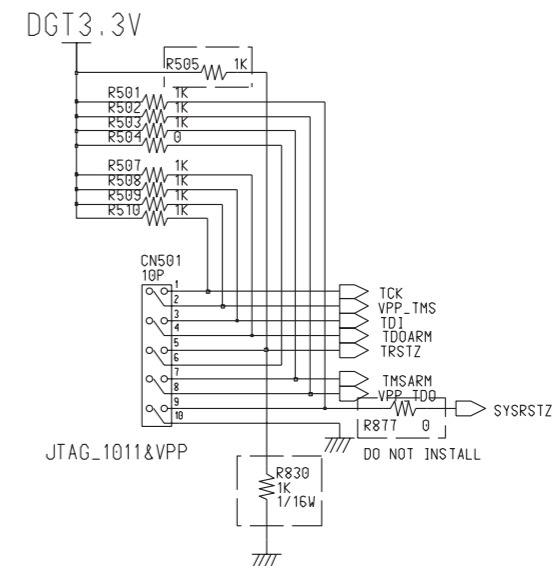
For Ballaster by Tho/Osr



For Ballaster by Philips

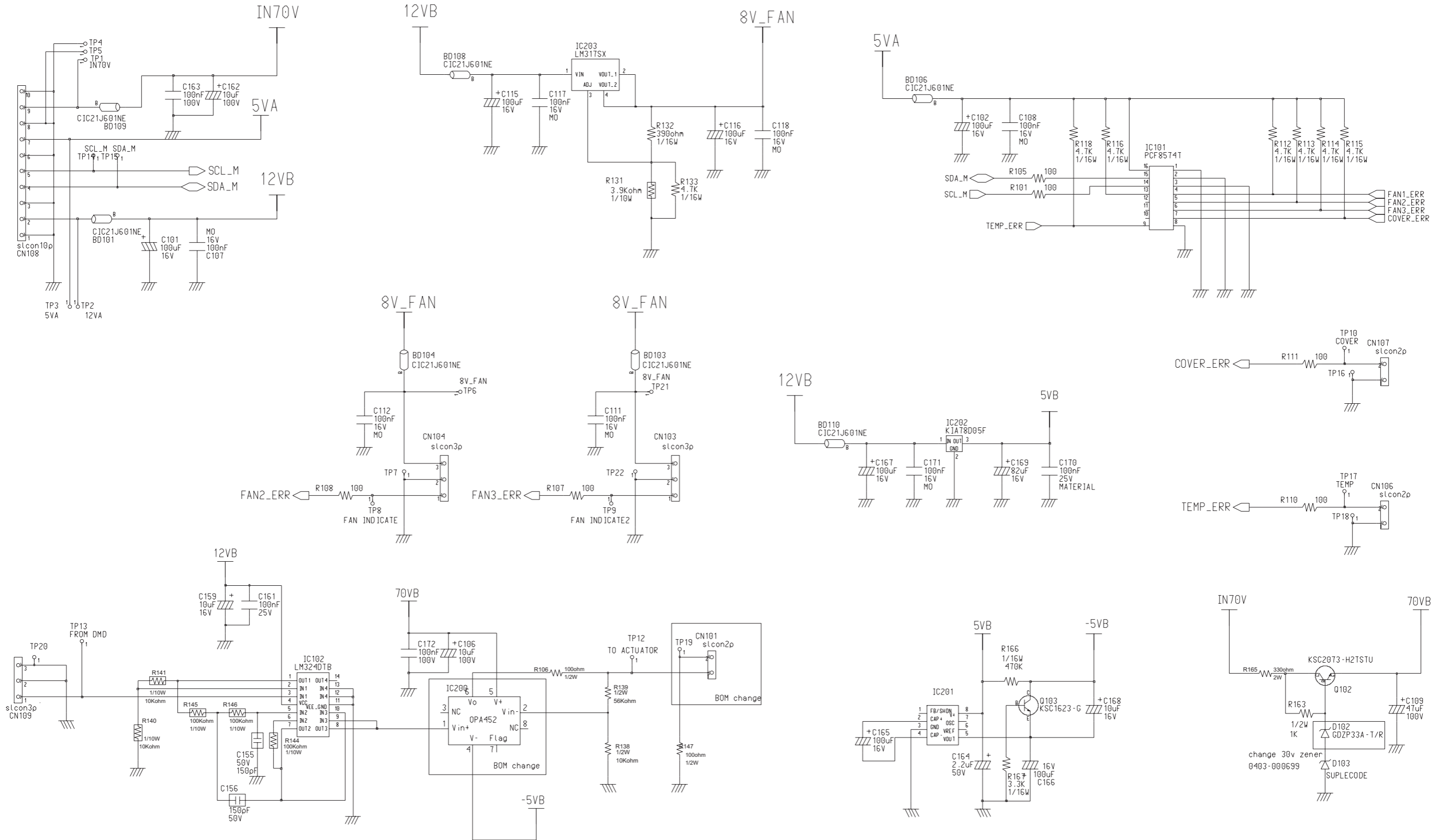


JTAG Configuration	R505	R830
Normal Operation	Not Installed	Installed
ARM-ICE Debug	Installed	Not Installed

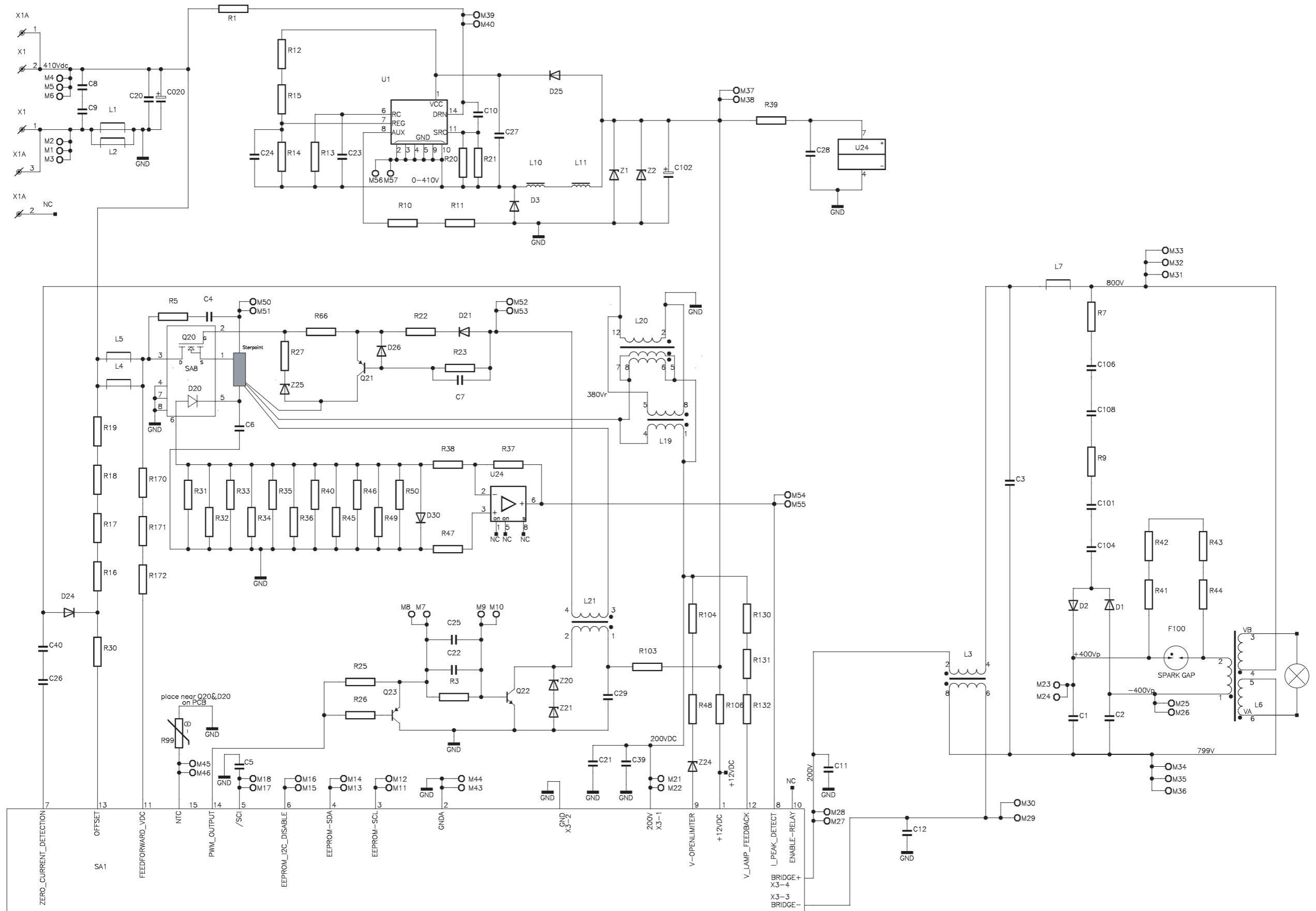


10-4 Actuator Board

( SIGNAL FROM ANALOG BOARD )

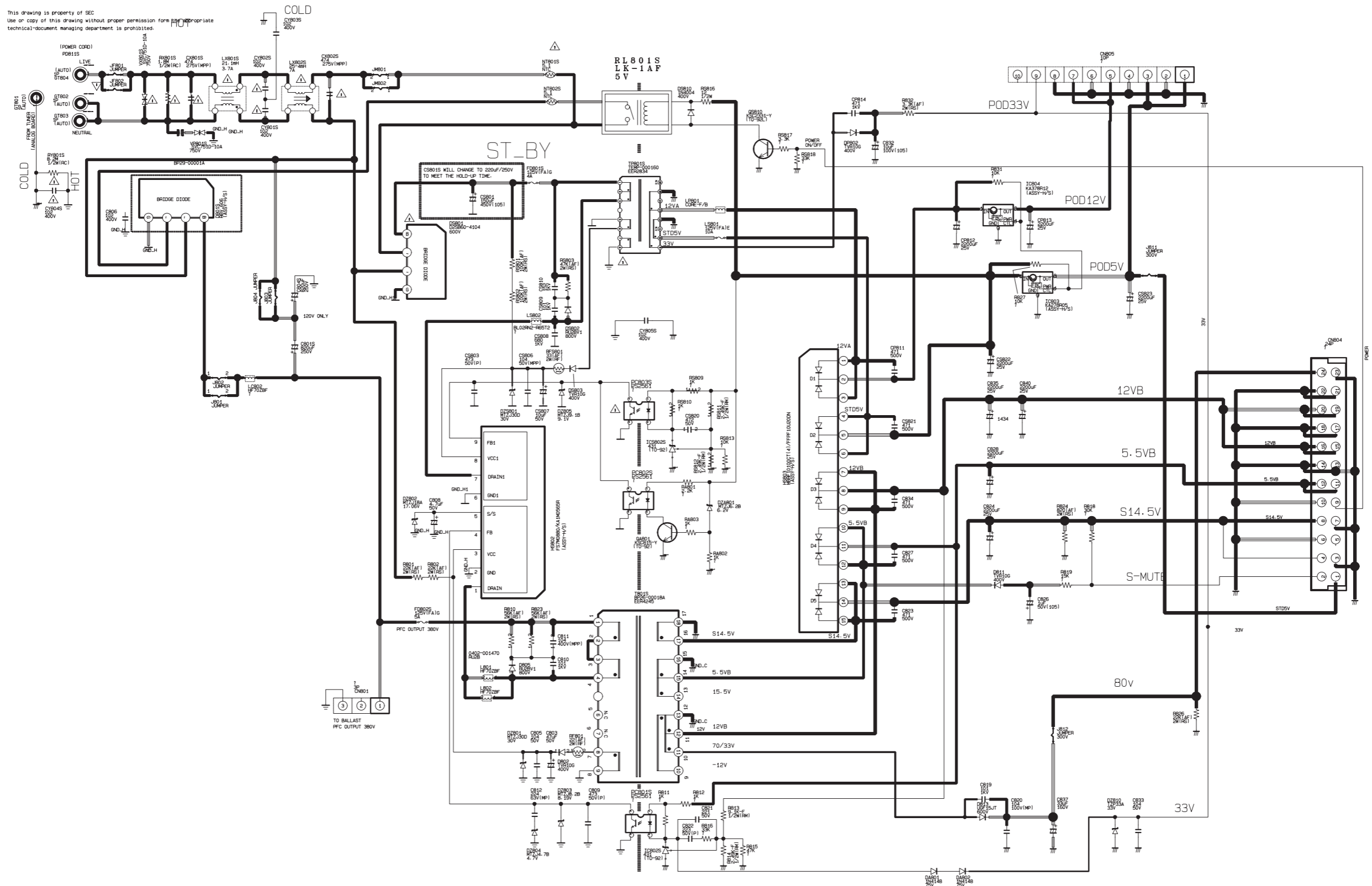


### 10-5 Ballast



# 10-6 Power Board

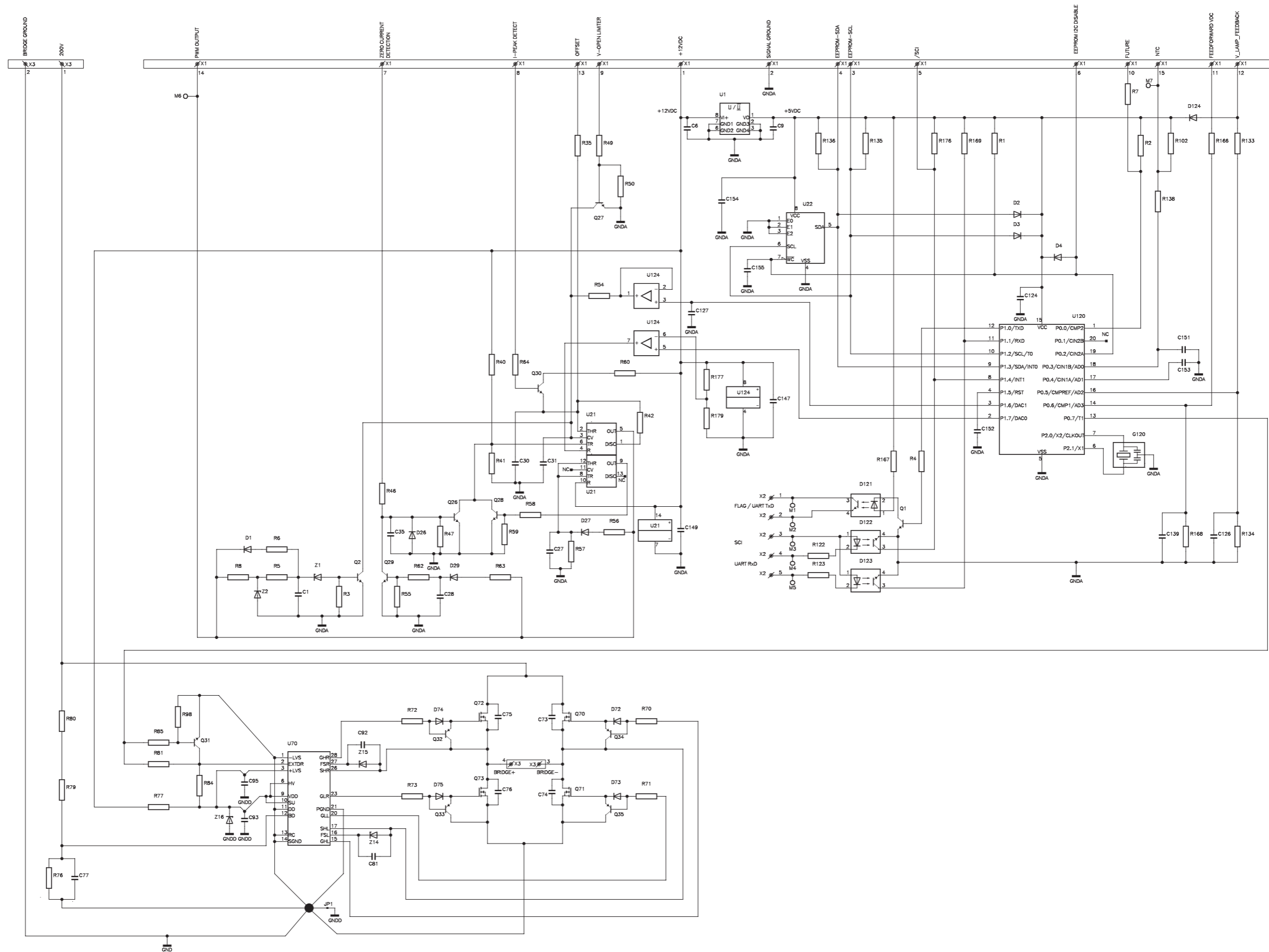
This drawing is property of SEC  
Use or copy of this drawing without proper permission from the appropriate  
technical-document managing department is prohibited.



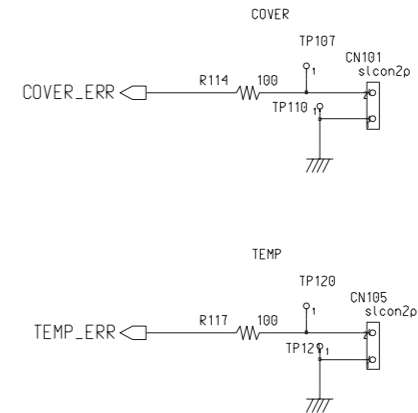
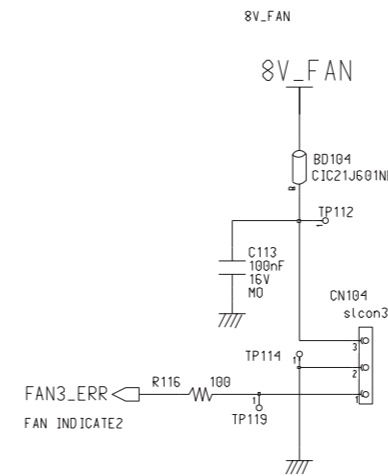
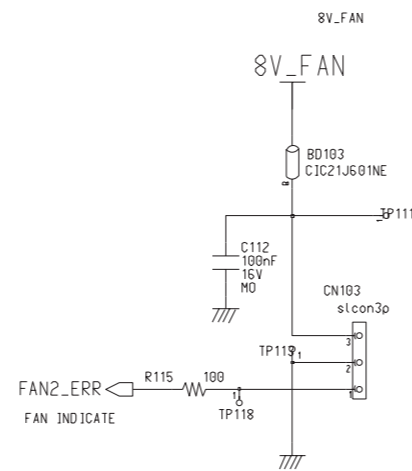
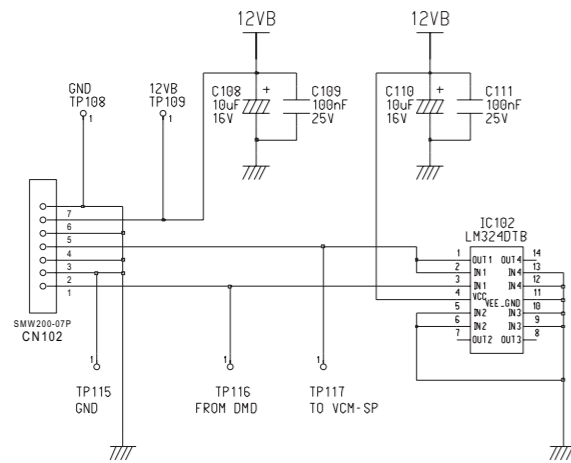
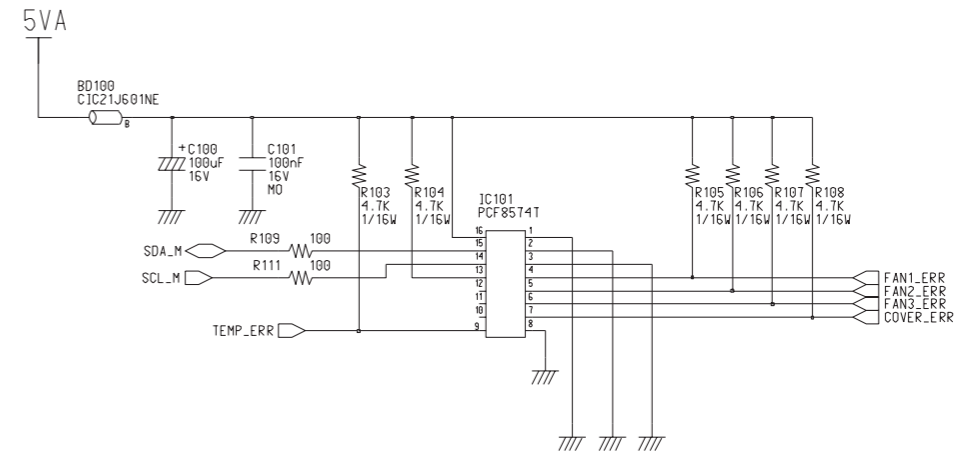
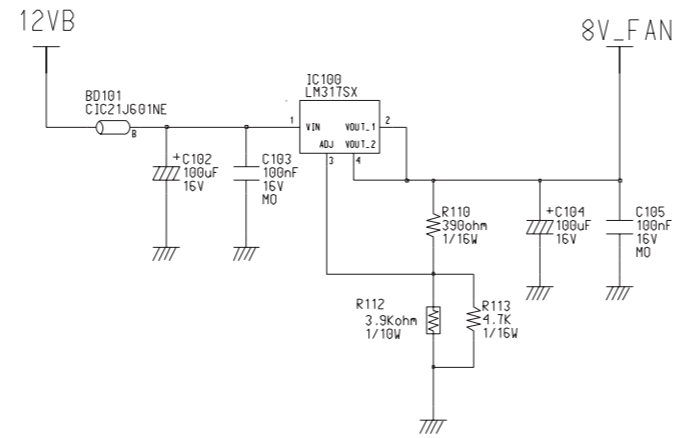
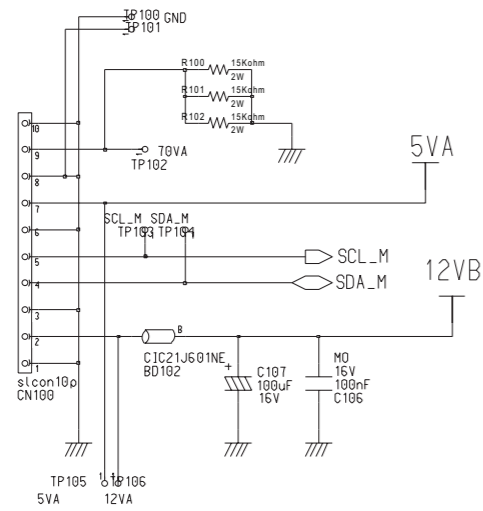
START-UP CIRCUIT MODIFY

HOT COLD

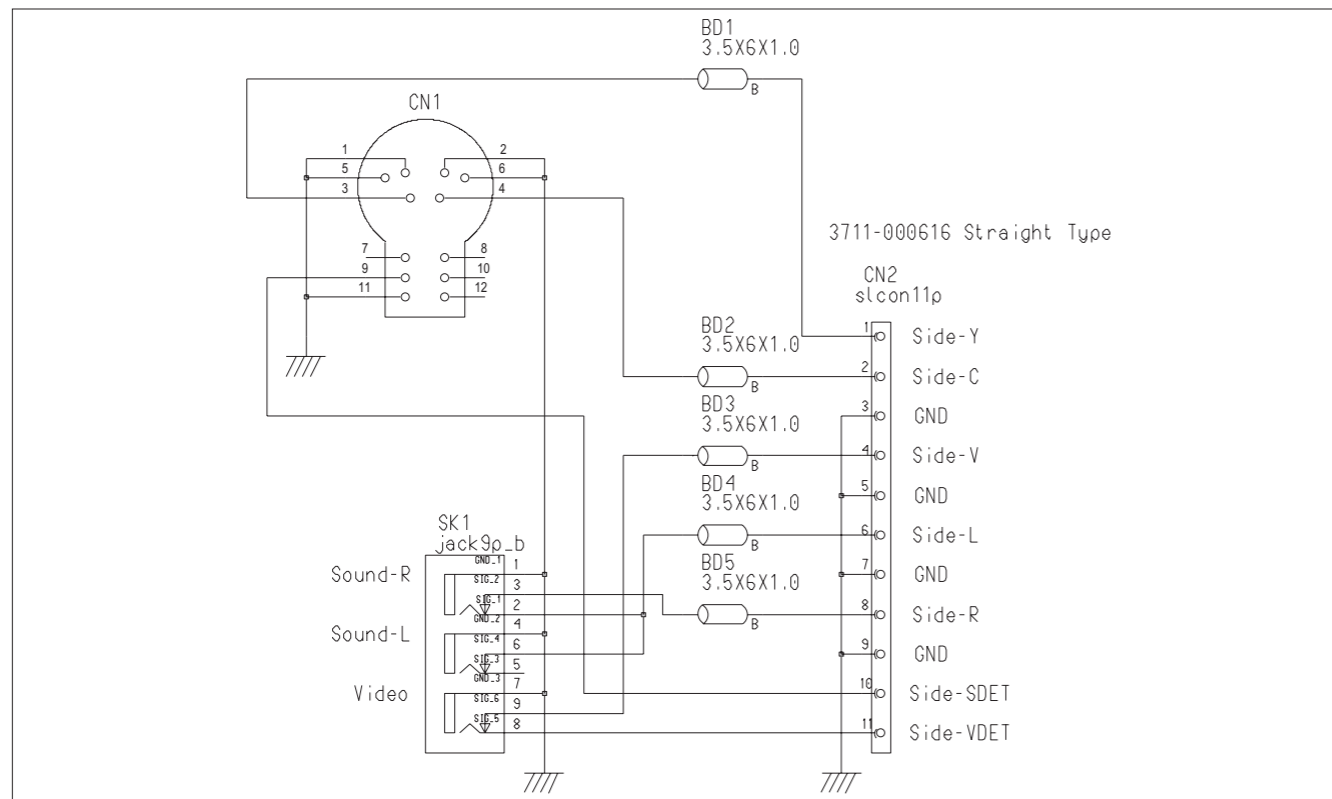
# 10-7 Combined Bridge and Control Module



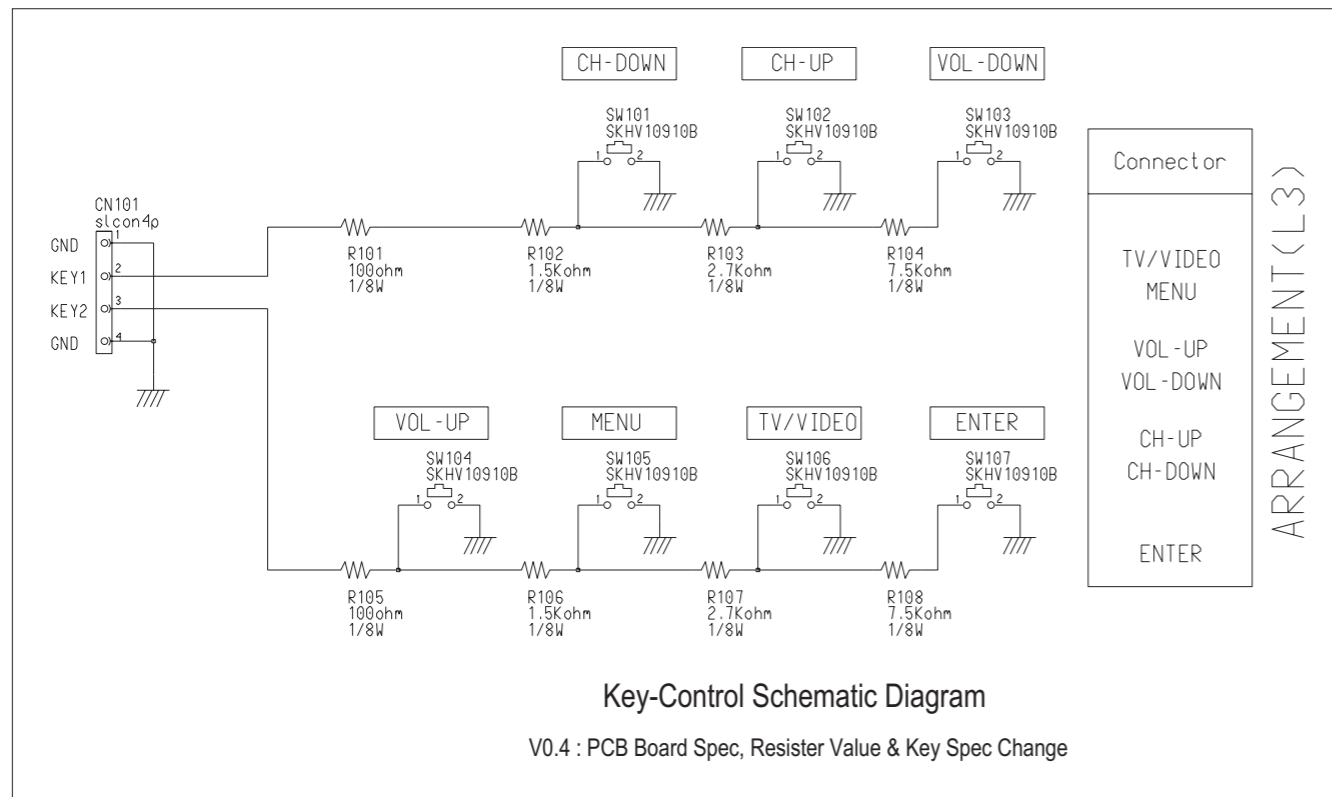
10-8 Detect



### 10-9 Side-AV



### 10-10 Key Control



### 10-11 RMC-LED

